

Solving the Instance Model View-Update Problem in AADL-OSATE

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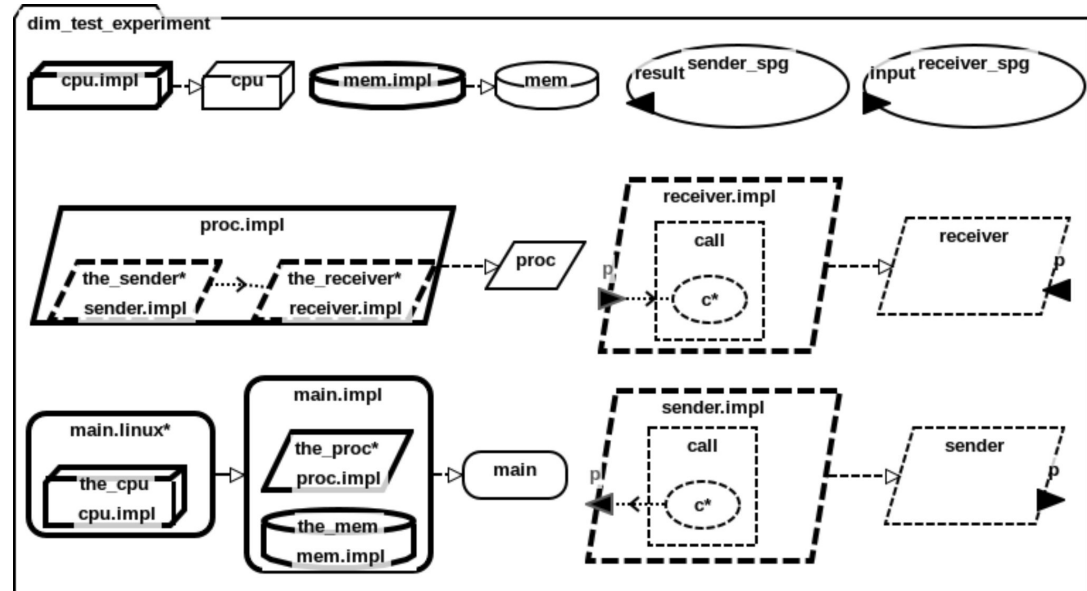
Telecom Paris, Institut Polytechnique de Paris, Palaiseau, France

AADL

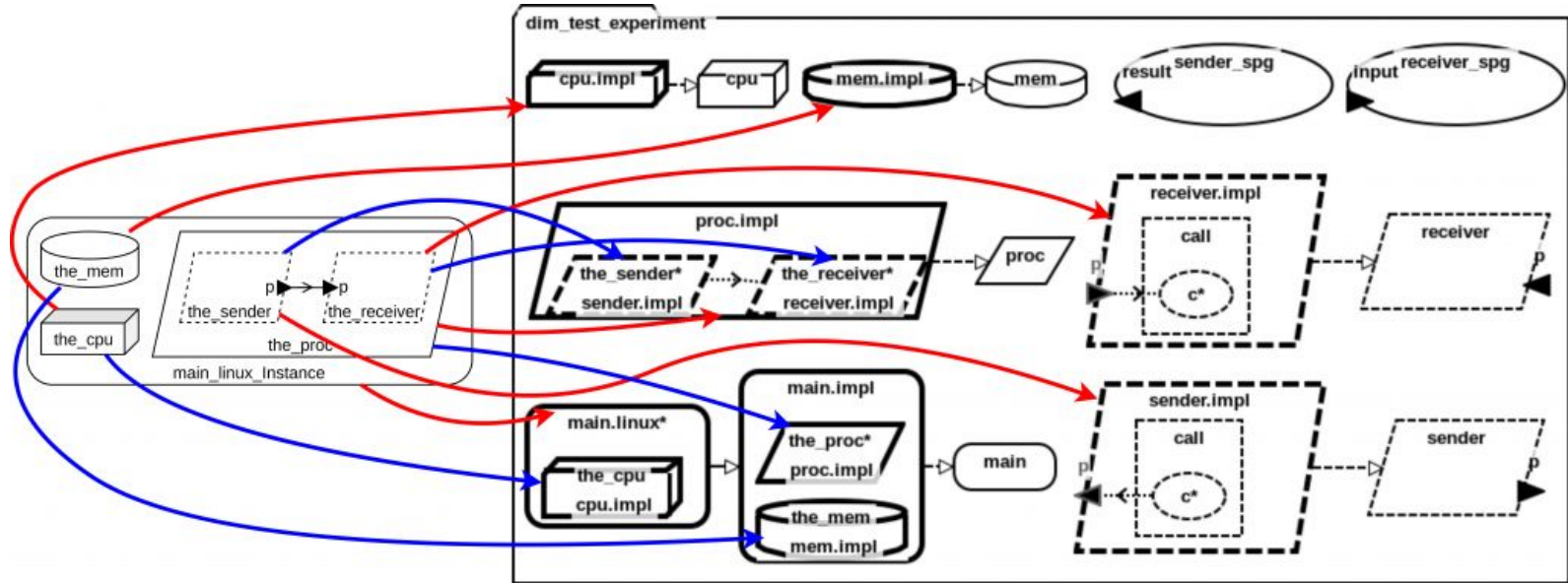
- Architecture Analysis and Design Language
 - SAE Standard AS5506D
 - to model real-time embedded systems composed of software and physical execution platform components tightly coupled with actuators and sensors to interact with their environments (Cyber-Physical Systems!)
 - scheduling/flow-control analyses
 - code generation for embedded platforms
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- Open-Source AADL Tool Environment (OSATE)
 - Eclipse IDE plugin

AADL Constructs

- Components
 - Classifiers
 - Type
 - Implementation
 - Extensions
 - Refinements
- Features
 - Refinements
- Connections
 - Refinements



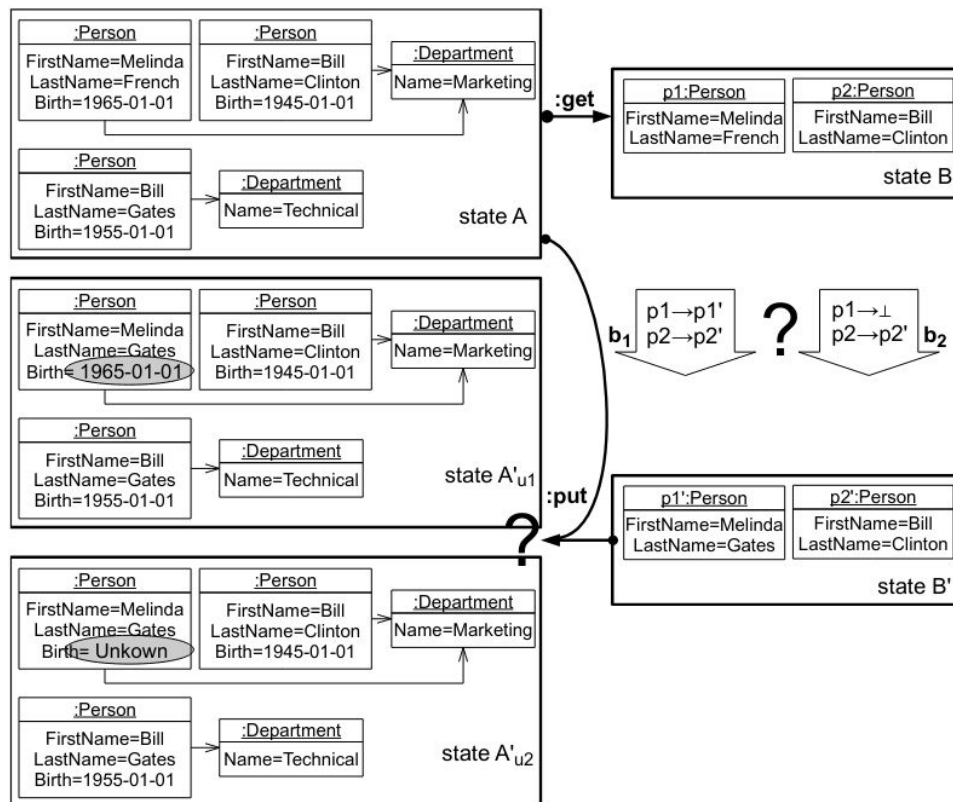
OSATE Instance Model



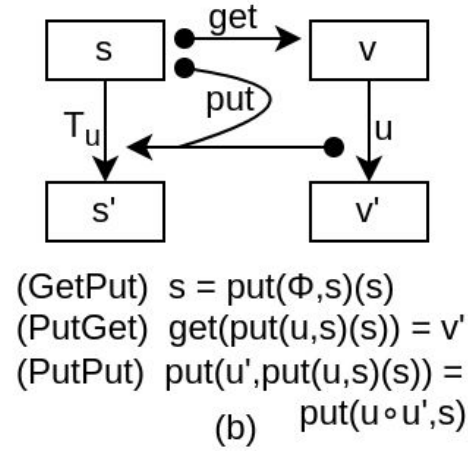
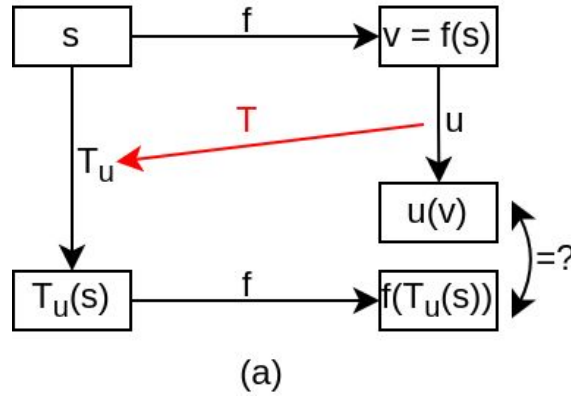
Instance Model

Declarative Model

View-Update Problem



Delta-based Lens



(GetPut) $s = \text{put}(\Phi, s)(s)$
 (PutGet) $\text{get}(\text{put}(u, s)(s)) = v'$
 (PutPut) $\text{put}(u', \text{put}(u, s)(s)) = \text{put}(u \circ u', s)$

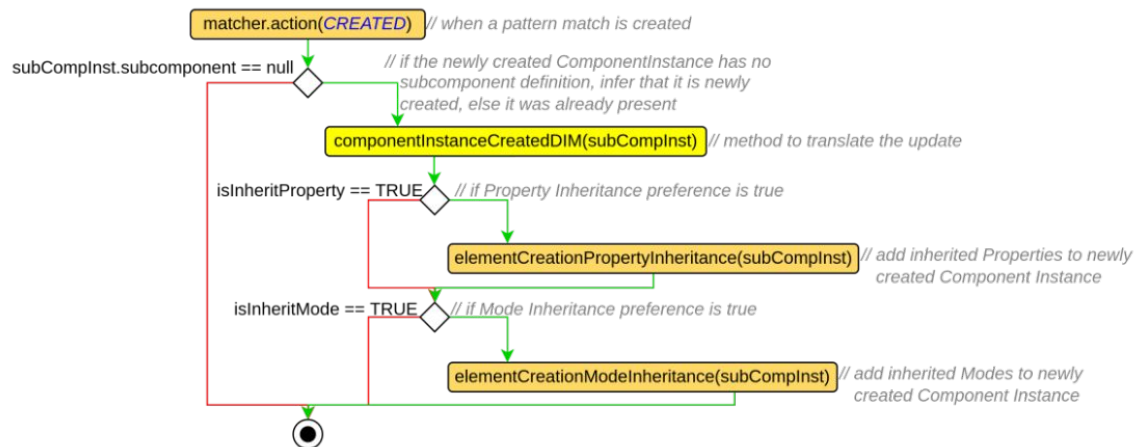
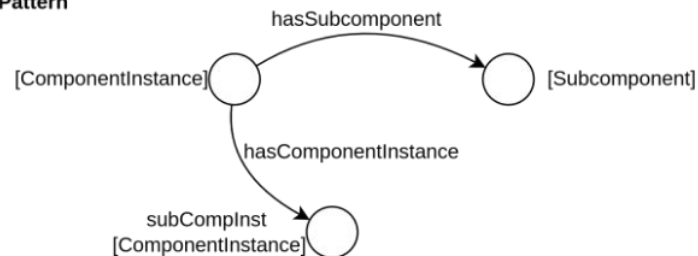
s : Model State
 f : View-Generating Function
 v : View-State
 u : View-Update
 T : Translation

\Rightarrow Declarative Model
 \Rightarrow Instantiation
 \Rightarrow Instance Model
 \Rightarrow Refinement
 \Rightarrow Deinstantiation

OSATE-DIM

- OSATE Declarative-Instance Mapping
- Eclipse/OSATE-based plugin
- Graph Transformations
 - VIATRA
 - Graphical Queries
 - Model Transformation Rules

Graphical Query Pattern

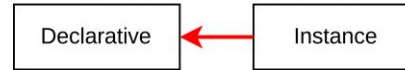


OSATE-DIM Values/Aims

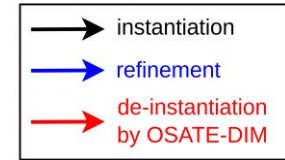
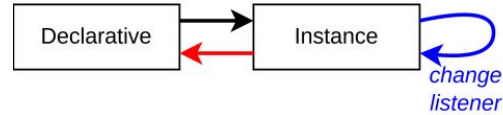
- Maximum Information Preservation
- Least/Minimal Change
- Very-well behaved lens (3 laws)
 - No extraneous model updates.
 - Equality of updated-model state with updated view-state
 - Composability of updates
- Flexibility
 - Scenarios
 - Preferences

Transformation Scenarios

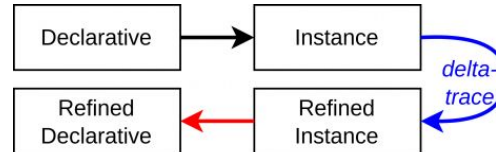
State-based



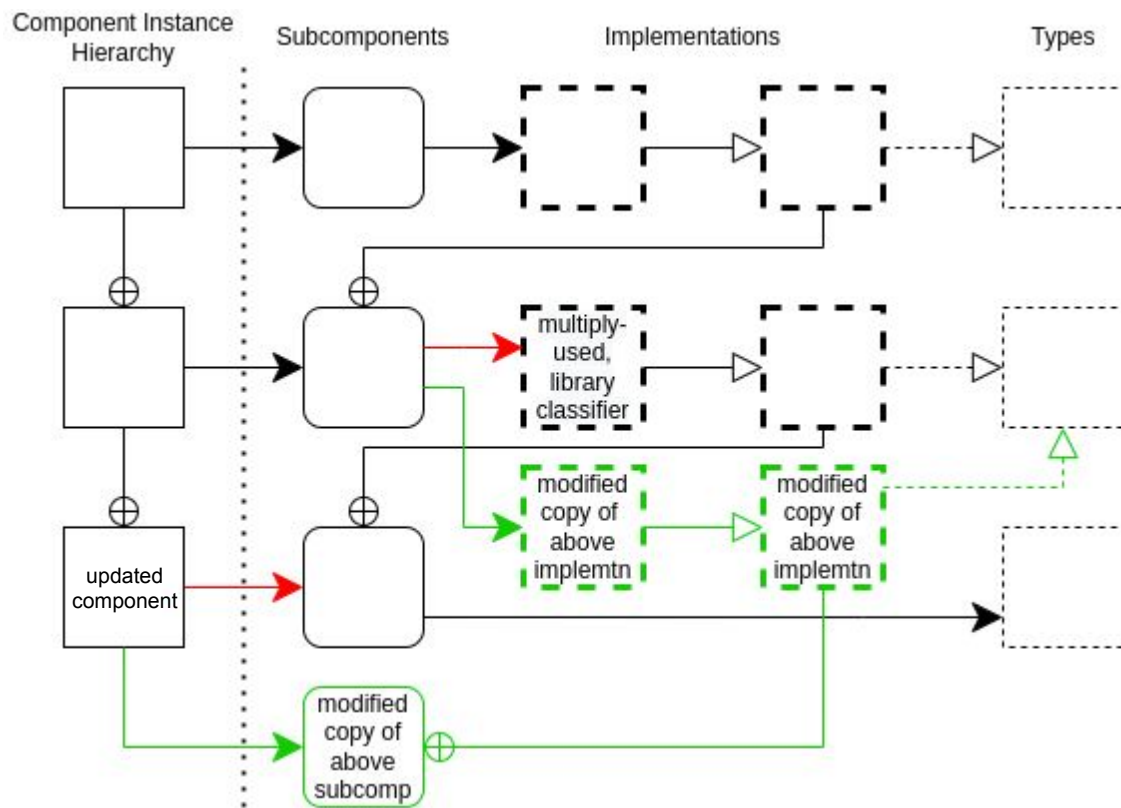
Delta-based with In-place refinement



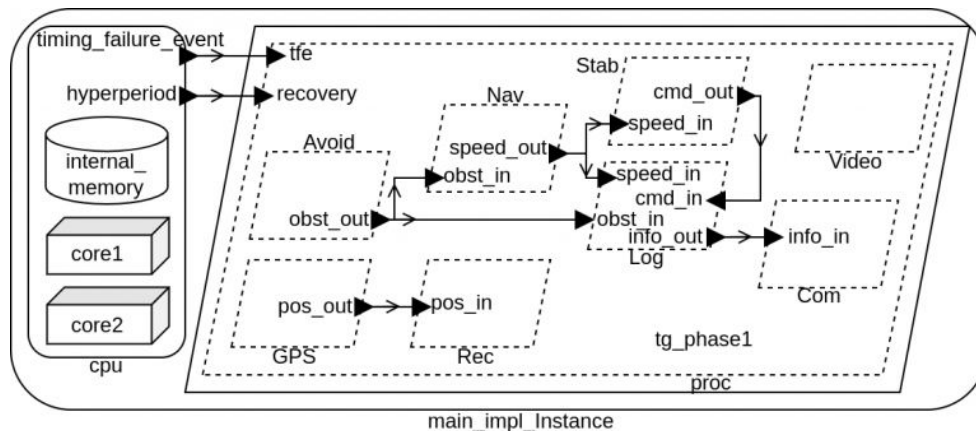
Delta-based with Out-of-place refinement



Update Propagation

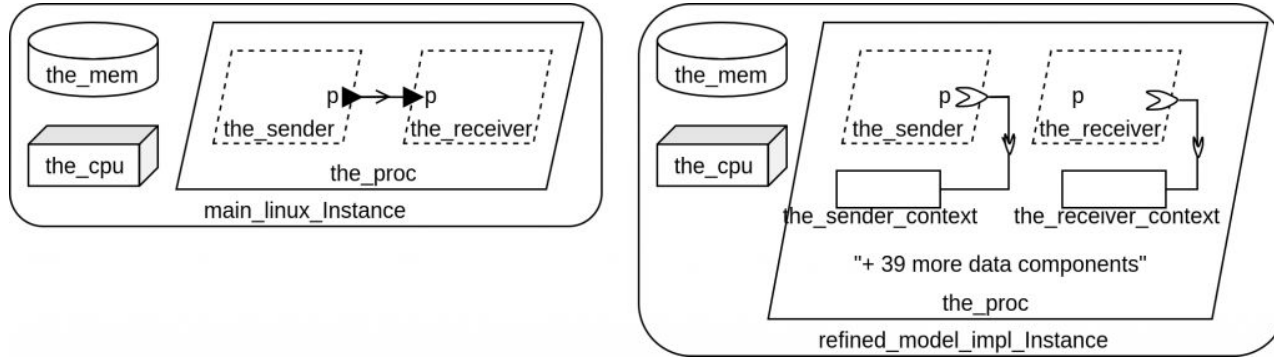


Case Study: MC-DAG



- Addition of *Property Associations* (RAMSES::Execution_Slots) for each *Thread*.
- Contain static scheduling tables for each *Thread* in different *Modes* LO and HI.
- Properties also reference the core and memory binding, not just static data.

Case Study: RAMSES



- Addition of 41 *Data Components* to a *Process Component*, which are shared by two threads.
- The *Port Features* interfacing the two threads with each other are changed to *Data Access* kinds.
- New *Data Access Connections* are also added between the shared *Data Components* and the *Threads*.
- The added *Data Components* have varying numbers of *Properties*, and the total number of newly added properties is 122.
- the name of the top-*System Instance* is changed to “refined_model_impl_Instance”

Demonstration