

Q IDLE BUSY
[0, 1, 0]



ARR

[1, 1, 0]



ARR

[2, 1, 0]

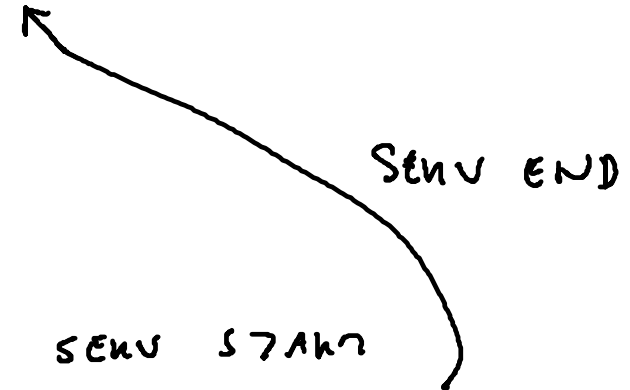


ARR

[3, 1, 0]

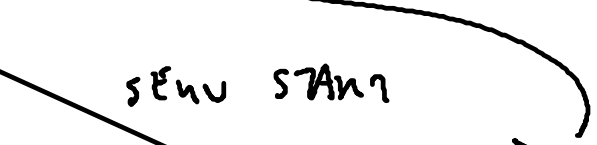


⋮



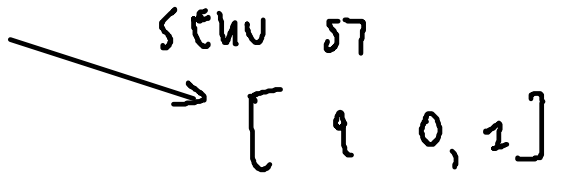
SEND START

[0, 0, 1]



SEND START

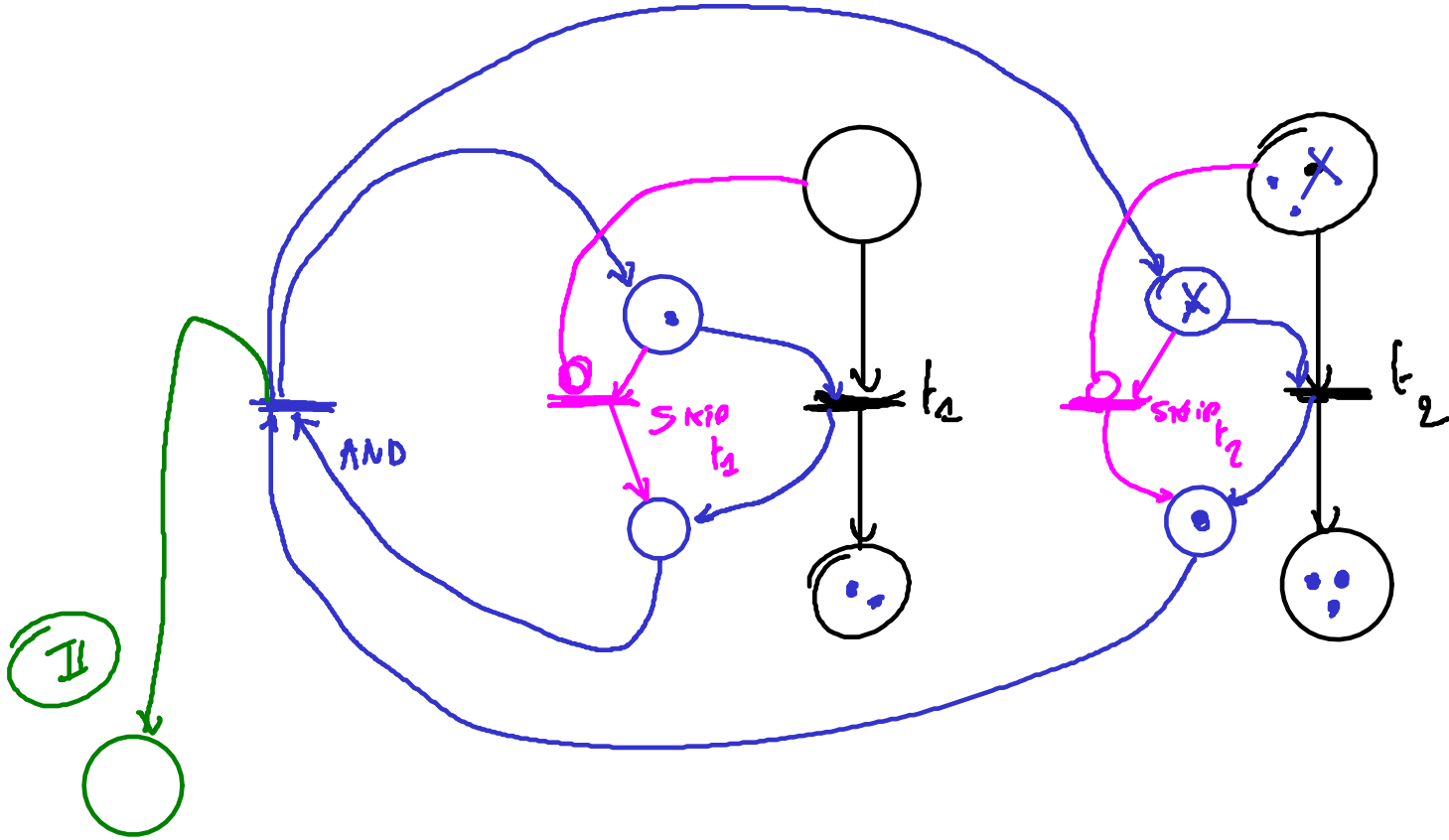
[1, 0, 1]



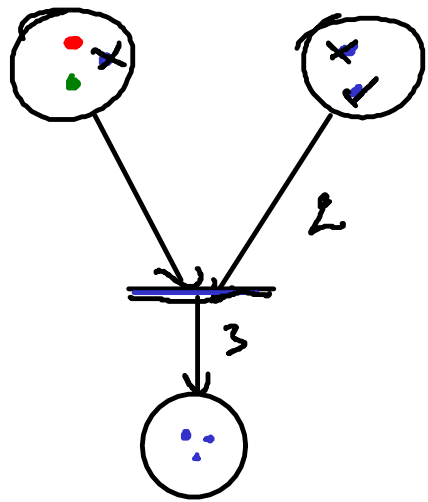
SEND START

[2, 0, 1]

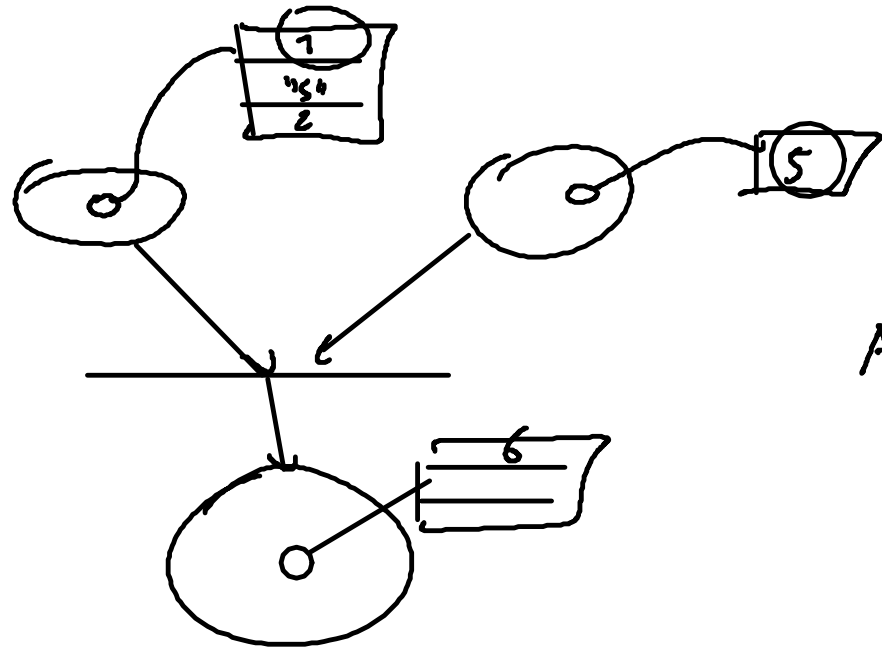
①



TIME SLICE
DT-CSD



CPN



ACG

SYNTHESIS