# Developing Reactive Systems using Statecharts

Modelling of Software-Intensive Systems

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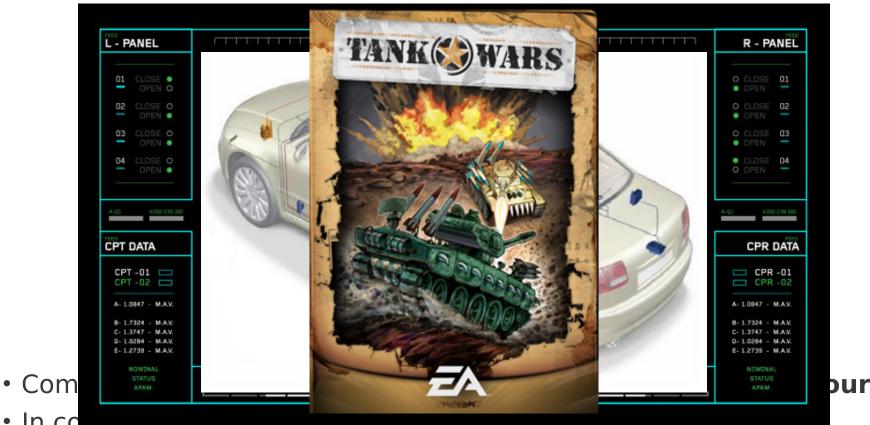






# Introduction

#### Reactive Systems



• In contrast to transformational systems, which take input and eventually, produce output

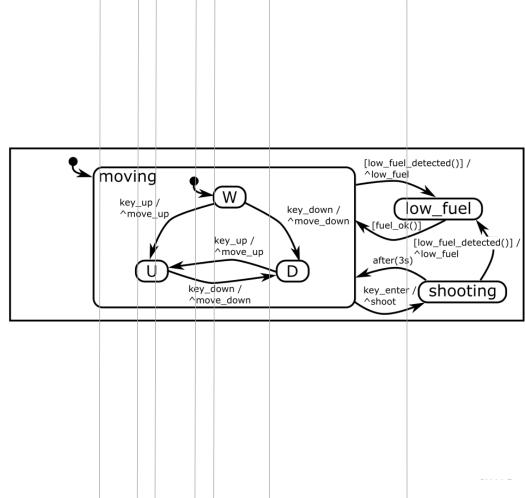
## Modelling Reactive Systems

- Interaction with the environment: reactive to events
- Autonomous behaviour: timeouts +
- System behaviour: modes os istoo low-level "how" concurrent units
   Use programing language (and "what "what "his a tentormage + threads and program spropriate formage + threads and program spropriate written with threads, semanhores and matexes are incomprehable."

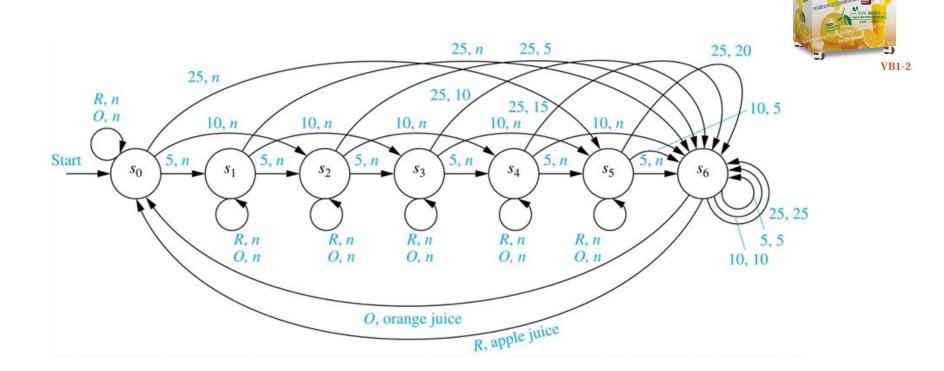
#### Discrete-Event Abstraction



behavioural model



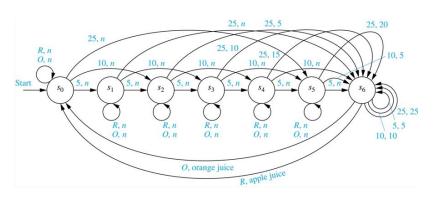
#### State Diagrams



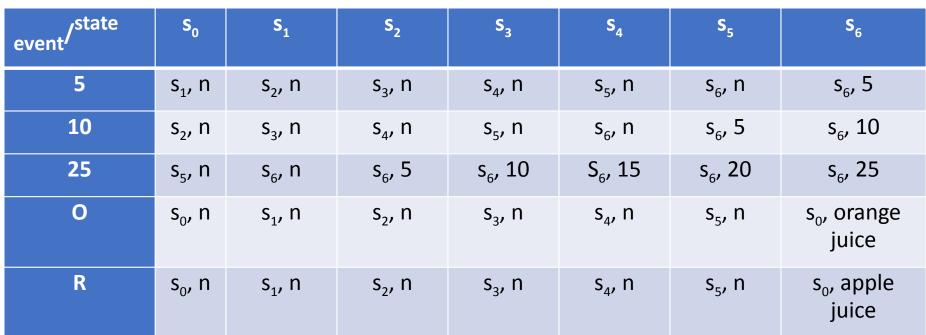
Fresh

- All states are explicitly represented (unlike Petrinets, for example)
- Flat representation (no hierarchy)
- Does not scale well: becomes too large too quickly to be usable (by humans)

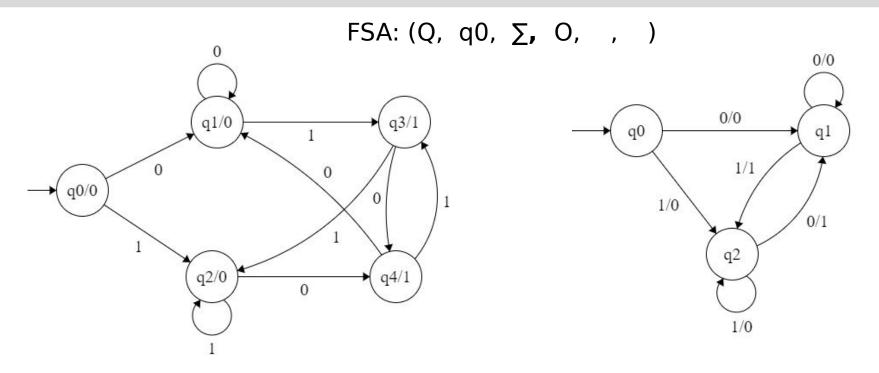
### Alternative Representation: Parnas Table



**VB1-2** 



### Mealy and Moore Machines



#### **Moore Machines**

 Output only depends on current state.

: Q O

Input stream: 10
 Output stream: 00

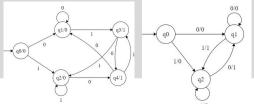
#### **Mealy Machines**

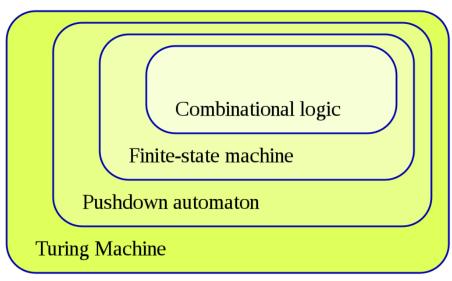
 Output depends on current state and on current input.

: Q x ∑ O

Input stream: 10
 Output stream: 01

### FSAs: Expressiveness





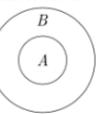
- Can be made Turing-complete data memory, control flow, branching
- Extend FSAs borrow semantics from Mealy and Moore machines

## Higraphs

#### **Euler Diagrams**

#### **Unordered Cartesian Product**

All A are B.



No A is B.

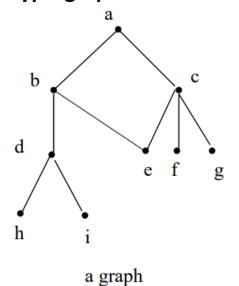
Some A is in B. Some A is not in B.



A = BC

topological notions for set union, difference, intersection

#### **Hypergraphs**



topological notion (syntax): connectedness e h

Hyperedges:

2<sup>x</sup> (undirected),

 $2^{x} \times 2^{x}$  (directed).

a hypergraph

 $X = \{a, b, ..., h\}$ 

David Harel. On Visual Formalisms. Communications of the ACM. Volume 31, No. 5. 1988. pp. 514 - 530.

# Higraphs

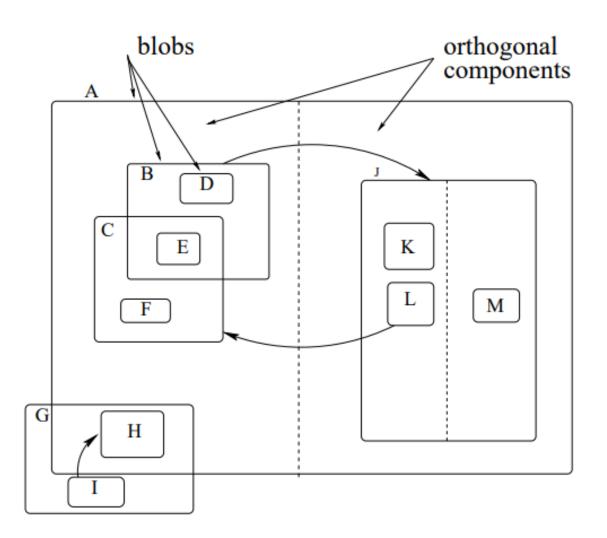
**Euler Diagrams** 

+

**Hypergraphs** 

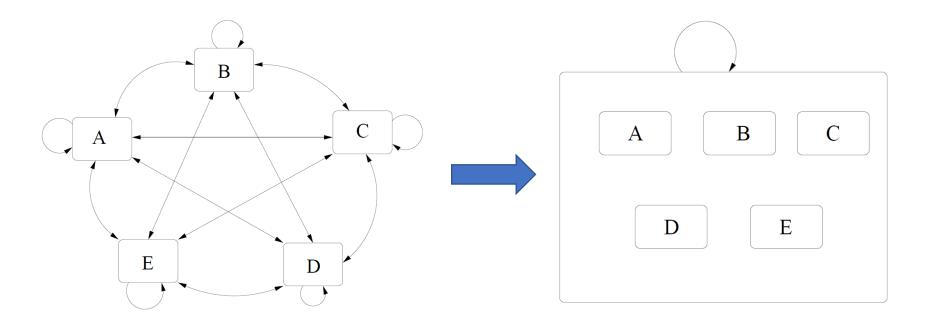
+

**Unordered Cartesian Product** 



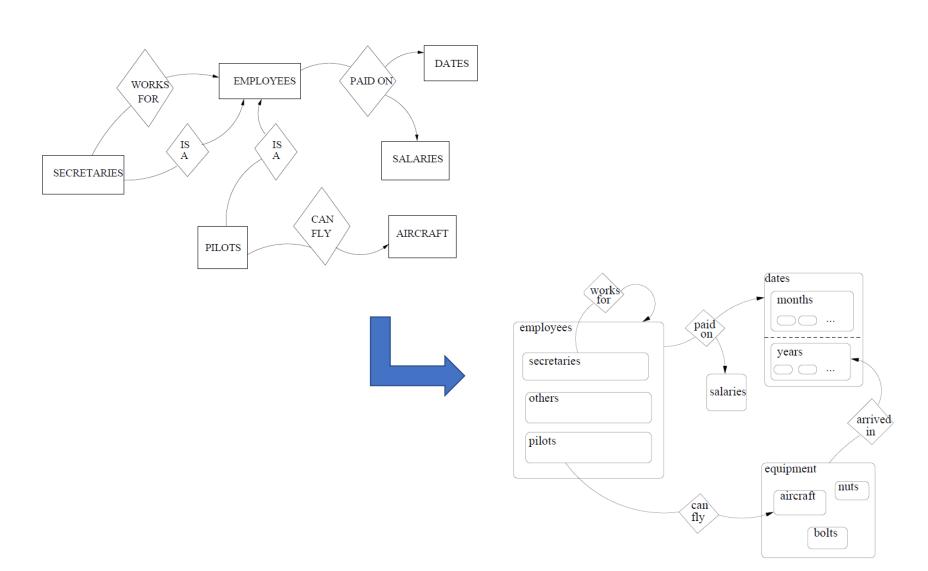
# Higraph: Examples

#### Clique

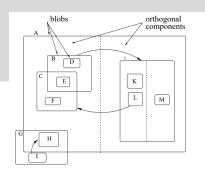


# Higraphs: Examples

#### ER-Diagrams



### Higraphs: Formal Definition



A higraph H is a quadruple

$$H = (B, E,$$

- B is a finite set of all unique blobs
- E is a set of *hyperedges*

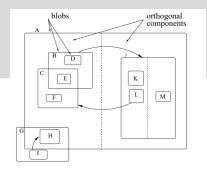
$$2^{B} \times 2^{B}$$

The subblob function

: B 
$$2^{B}$$

$$\sigma^{i+1}(x) = \bigcup_{y \in \sigma^{i}(x)} \sigma(y) \quad \sigma^{+}(x) = \bigcup_{i=1}^{n} \sigma^{i}(x)$$

### **Higraphs: Formal Definition**



Subblobs relation cycle-free

$$\times$$
 (X)

• The partitioning function  $\pi$  associates an equivalence relationship with x

B 
$$2^{B\times B}$$

- Equivalence classes  $\pi_i$  are orthogonal components of x  $\pi_1(x), \, \pi_2(x), \, ..., \, \pi_{kx}(x)$
- $k_x = 1$  means a single orthogonal component
- Blobs in different orthogonal components of x are disjoint

$$y,z$$
 (x):  $+(y)$   $+(z) =$ 

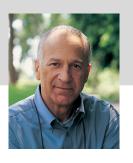
## **Higraphs Applications**

- Apply syntactic constructs to an existing modelling language.
- Add specific meaning to these constructs.
- Examples:
  - E-R diagrams
  - Dataflow/Activity Diagrams
  - Inheritance
  - Statecharts

#### **Statecharts**

- Visual (topological, not geometric) formalism
- Precisely defined syntax and semantics
- Many uses:
  - Documentation (for human communication)
  - Analysis (of behavioural properties)
  - Simulation
  - Code synthesis
  - ... and derived, such as testing, optimization, ...

## Statecharts History



- Introduced by David Harel in 1987
- Notation based on higraphs = hypergraphs + Euler diagrams + unordered Cartesian product
- Semantics extends deterministic finite state automata with:
  - Depth (Hierarchy)
  - Orthogonality
  - Broadcast Communication
  - Time
  - History
  - Syntactic sugar, such as enter/exit actions

## Statecharts History

- Incorporated in UML: State Machines (1995)
- More recent: xUML for semantics of UML subset (2002)
- W3 Recommendation: State Chart XML (SCXML) (2015)

https://www.w3.org/TR/scxml/

 Standard: Precise Semantics for State Machines (2019)

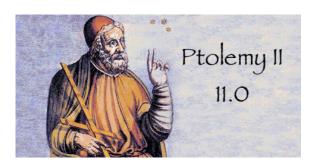
https://www.omg.org/spec/PSSM/

#### Statechart (Variants) Tools

# STATEMATE: A Working Environment for the Development of Complex Reactive Systems



https://www.ibm.com/us-en/marketplace/systems-design-rhapsody



https://ptolemy.berkeley.edu/ptolemyII/ptII11.0/index.htm



REAL-TIME
OBJECT-ORIENTED
MODELING





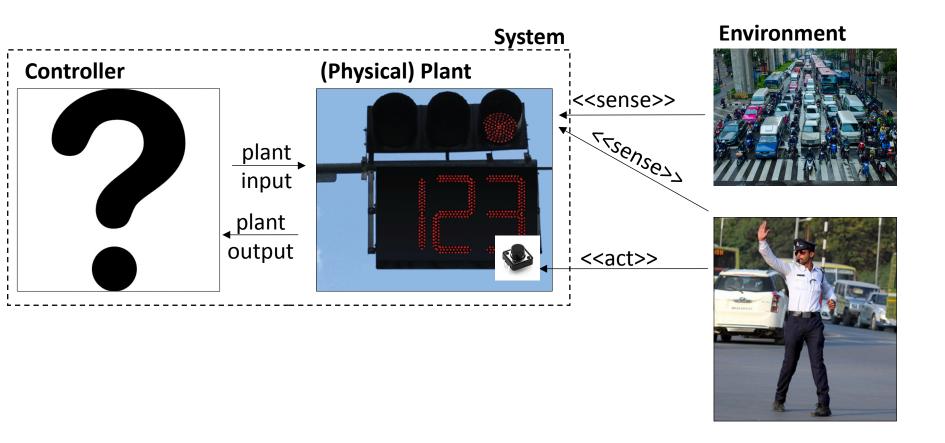
https://www.mathworks.com/products/stateflow.html



https://www.itemis.com/en/yakindu/state-machine/



# Running Example



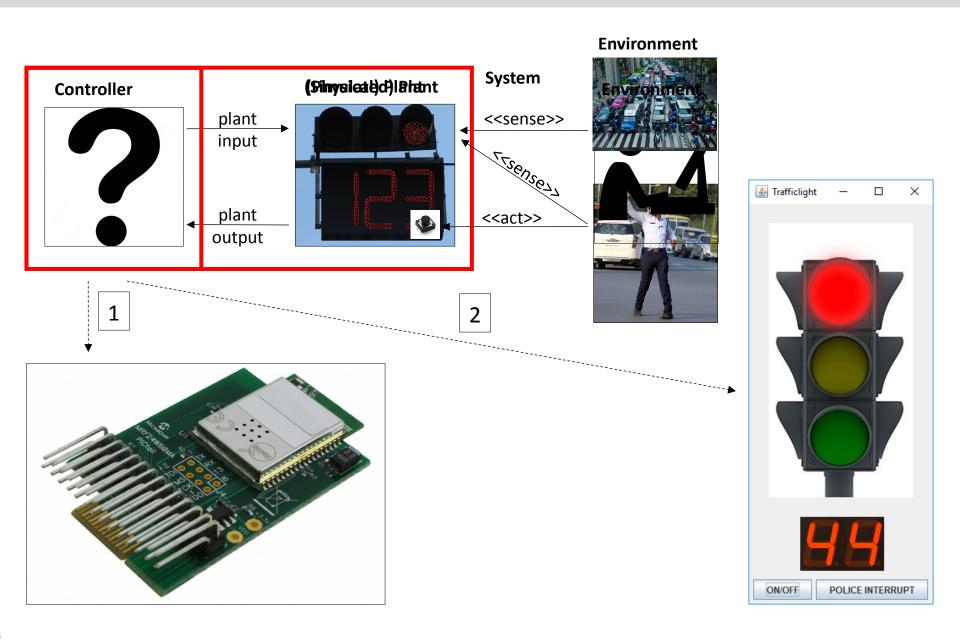
## What are we developing?

#### 

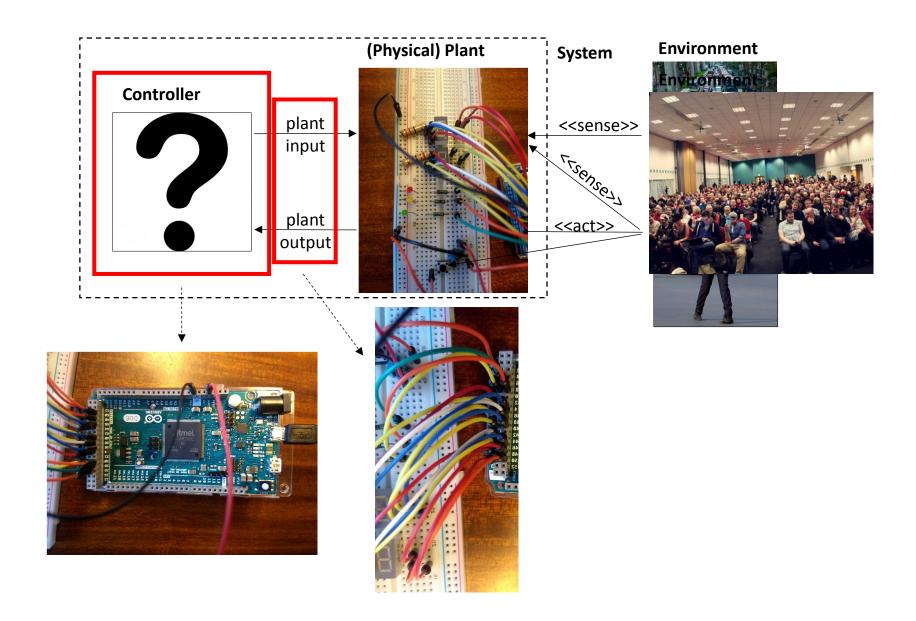
**Environment** 

- Autonomous (timed) behaviour
- Interrupt logic
- Orthogonal (traffic light/timer) behaviour
- Turn on/off traffic lights (red/green/yellow)
- Display counter value (three-digit)
- Change counter colour (red/green)
- Sense button presses

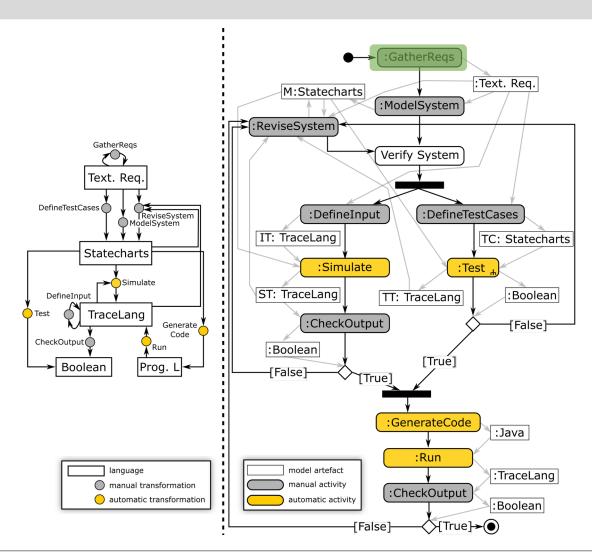
# Deployment (Simulation)



# Deployment (Hardware)



#### Workflow

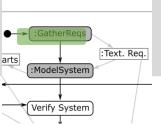


Hans Vangheluwe and Ghislain C. Vansteenkiste. A multi-paradigm modeling and simulation methodology: Formalisms and languages. In European Simulation Symposium (ESS), pages 168-172. Society for Computer Simulation International (SCS), October 1996. Genoa, Italy.

Levi Lúcio, Sadaf Mustafiz, Joachim Denil, Hans Vangheluwe, Maris Jukss. FTG+PM: An Integrated Framework for Investigating Model Transformation Chains. System Design Languages Forum (SDL) 2013, Montreal, Quebec. LNCS Volume 7916, pp 182-202, 2013.

#### Requirements

- R1: three differently coloured lights: red, green, yellow
- R2: at most one light is on at any point in time
- R3: at system start-up, the red light is on
- R4: cycles through red on, green on, and yellow on
- R5: red is on for 60s, green is on for 55s, yellow is on for 5s
- R6: time periods of different phases are configurable.
- R7: police can interrupt autonomous operation
  - Result = blinking yellow light (on -> 1s, off -> 1s)
- R8: police can resume an interrupted traffic light
  - Result = light which was on at time of interrupt is turned on again
- R9: traffic light can be switched on and off and restores its state
- R10: a timer displays the remaining time while the light is red or green; this timer decreases and displays its value every second.
   The colour of the timer reflects the colour of the traffic light.





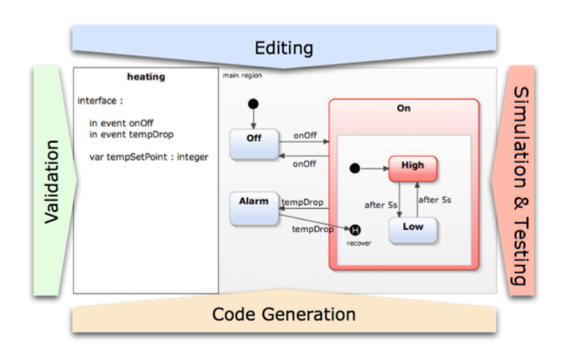
# YAKINDU Statechart Tools

Statecharts made easy...



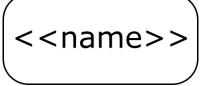
#### What are YAKINDU Statechart Tools?

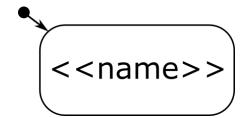
YAKINDU Statechart Tools provides an **integrated modeling environment** for the specification and
development of **reactive**, **event-driven systems**based on the concept of statecharts.



# The Statecharts Language

#### **States**





being in a state

= state <<name>> is active

= the system is in
configuration << name>>

initial state
exactly one per
 model

"entry point"

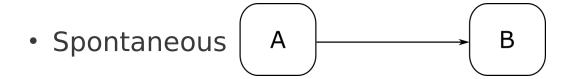
#### **Transitions**



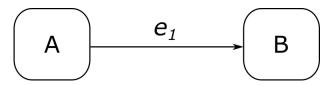
- Model the dynamics of the system:
  - when
    - the system is in state A and
    - the event is received
  - then
    - 1. output action is evaluated and
    - 2. the new active state becomes B

#### Transitions: Events

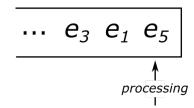
event(in\_params) / output\_action(out\_params)



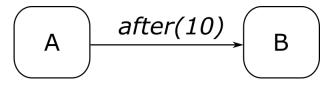
Input Event



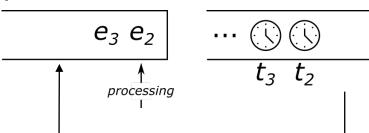
#### queue of event notices



After Event

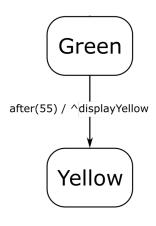


#### queue of event notices



### Transitions: Raising Output Events

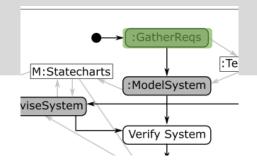
event(in\_params) / output\_action(out\_params)



Syntax for output action:

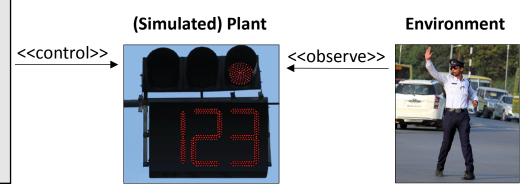
^output\_event
means "raise the event output\_event (to the environment)"

#### Exercise 1 - Requirements

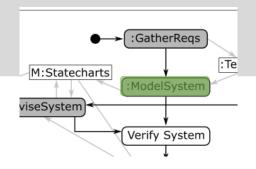


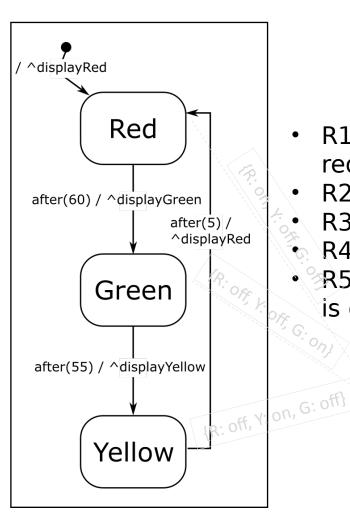
Your model here.

- R1: three differently coloured lights: red (R), green (G), yellow (Y)
- R2: at most one light is on at any point in time
- R3: at system start-up, the red light is on
- R4: cycles through red on, green on, and yellow on
- R5: red is on for 60s, green is on for 55s, yellow is on for 5s

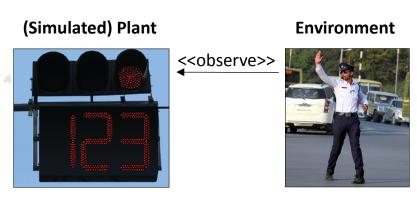


#### Exercise 1 - Solution



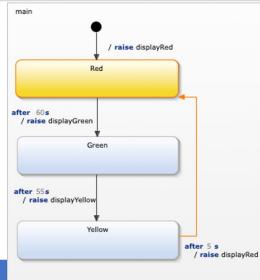


- R1: three differently coloured lights: red (R), green (G), yellow (Y)
- R2: at most one light is on at any point in time
- R3: at system start-up, the **red** light is on
- R4: cycles through red on, green on, and yellow on
- R5: **red** is on for 60s, **green** is on for 55s, **yellow** is on for 5s





## Exercise 1 - Solution



| requirement   | modelling approach   |
|---|--|
| R1: three differently coloured <b>lights</b> : red (R), green (G), yellow (Y) | For each colour a <b>state</b> is defined. Transitions that lead to a state raise the proper out event which interacts with the plant. |
| R2: at most one light is on at any point in time                              | The states are all contained in a single region and thus a exclusive to each other ("or" states).                                      |
| R3: at system start-up, the red light is on                                   | The entry node points to state Red and the entry transition raises the event displayRed.   |
| R4: cycles through red on, green on, and yellow on                            | The transitions define this cycle.   |
| R5: red is on for 60s, green is on for 55s, yellow is on for 5s               | Time events are specified on the transitions.  |

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## Data Store

### **Full** System State



#### DataStore

-  $var_1$ :  $t_1 = val_1$ 

-  $var_2$ :  $t_2 = val_2$ 

-  $var_n$ :  $t_n = val_n$ 

being in a state

= state <<name>> is active

= the system is in
configuration << name>>

data store **snapshot** 

= variable values

=

full system state

### Full System State: Initialization



```
DataStore
```

```
- var_1: t_1 = val_1

- var_2: t_2 = val_2

- var_n: t_n = val_n
```

```
1 int main() {
2
3 }
```

initial state
exactly one per
 model
"entry point"

provide **default value** for each
variable

"initial snapshot"

Compare:
C++ initialization
implicit state
(program counter)

+ data store

#### Transitions: Guards

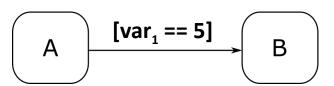
event(in\_params) [guard] / output\_action(out\_params)

Modelled by "guard expression" (evaluates to Boolean) in some appropriate language

Spontaneous [True]



Data Store
 Variable Value



DataStore
-  $var_1$ :  $t_1 = val_1$ 

-  $var_2^1$ :  $t_2^1 = val_2^1$ 

- var<sub>n</sub>: t<sub>n</sub> = val<sub>r</sub>

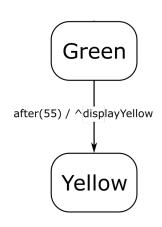
Parameter Value

#### **Transitions: Output Actions**

event(params) [guard] / output\_action(params)

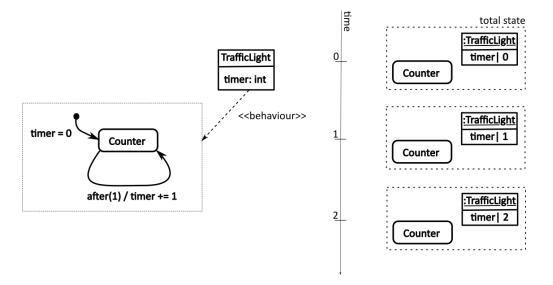
#### **Output Event**

 $\hat{p}_1, p_2, ..., p_n$ 



**Assignment** (to the non-modal part of the state)

 by action code in some appropriate language



#### **Transitions**

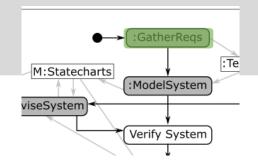


- Model the dynamics of the system:
  - when
    - the system is in state A and
    - event is received and
    - guard evaluates to True
  - then
    - output\_action is evaluated and
    - 2. the new active state becomes B

## Exercise 2

Add data stores

### Exercise 2 - Requirements



Your model here.

- R6': During the last 6 seconds of red being on, the traffic light announces to go to green by blinking its yellow light (1s on, 1s off) while leaving its red light on.
- R6: The time period of the different phases should be configurable.

#### TrafficLight

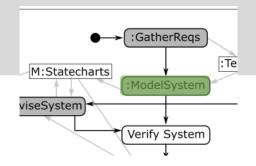
- counter: Integer = 0
- green: Boolean = false
- red: Boolean = false
- yellow: Boolean = false

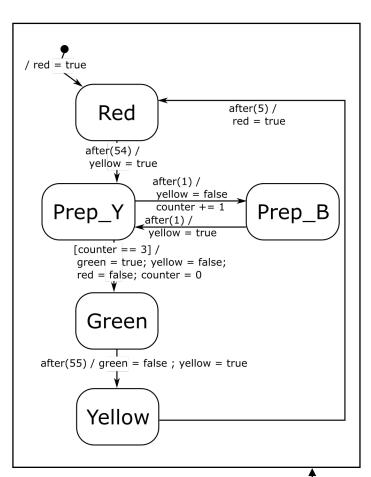
#### Make sure that:

- the values of the variables reflect which lights are on/off
- you use at least one conditional transition

<<behavior>>

#### **Exercise 2: Solution**





- R6': During the last 6 seconds of red being on, the traffic light announces to go to green by blinking its yellow light (1s on, 1s off) while leaving its red light on.
- R6: The time period of the different phases should be configurable.

#### TrafficLight

- counter: Integer = 0
- green: Boolean = false
- red: Boolean = false
- yellow: Boolean = false

<<behavior>>

## Statechart Execution

#### Run-To-Completion Step

- A Run-To-Completion (RTC) step is an *atomic* execution step of a state machine.
- It transitions the state machine from a valid state configuration into the next valid state configuration.
- RTC steps are executed one after the other they must not interleave.
- New incoming events cannot interrupt the processing of the current event and must be stored in an event queue

### Flat Statecharts: Simulation Algorithm (1)

```
1 simulate(sc: Statechart) {
```

### Flat Statecharts: Simulation Algorithm (2)

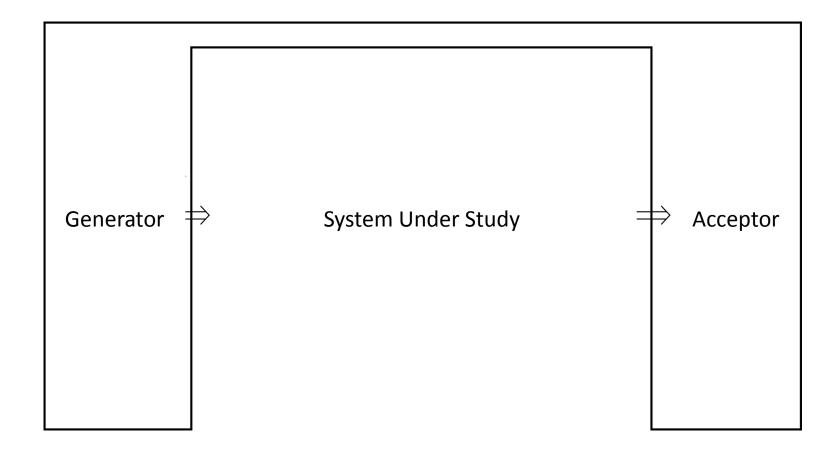
```
simulate(sc: Statechart) {
        input events = initialize queue()
        output events = initialize queue()
        timers
                      = initialize set()
                      = sc.initial state
        curr state
        for (var in sc.variables) {
 6
            var.value = var.initial value
8
        while (not finished()) {
            curr event = input events.get()
10
            while (not quiescent()) {
11
                enabled transitions = find enabled transitions(curr state, curr event, sc.variables)
12
                chosen transition = choose one transition(enabled transition)
13
                cancel_timers(curr_state, timers)
14
                curr_state = chosen_transition.target
15
                chosen transition.action.execute(sc.variables, output events)
16
                start timers(curr state, timers)
17
18
19
20
```

### Flat Statecharts: Simulation Algorithm (3)

```
simulate(sc: Statechart) {
        input events = initialize queue()
        output events = initialize queue()
 3
        timers
                      = initialize_set()
                      = sc.initial state
        curr state
        for (var in sc.variables) {
 6
            var.value = var.initial value
        while (not finished()) {
            curr_event = input_events.get()
10
            enabled_transitions = find_enabled_transitions(curr_state, curr_event, sc.variables)
11
            while (not quiescent()) {
12
                chosen transition = choose one transition(enabled transition)
13
                cancel timers(curr state, timers)
14
                curr state = chosen transition.target
15
                chosen transition.action.execute(sc.variables, output_events)
16
                start_timers(curr_state, timers)
17
                enabled transitions = find enabled transitions(curr state, sc.variables)
18
19
20
21
```

# Testing Statecharts

### **Testing Statecharts**



Zeigler BP. Theory of modelling and simulation. New York: Wiley-Interscience, 1976.

Mamadou K. Traoré, Alexandre Muzy, Capturing the dual relationship between simulation models and their context, Simulation Modelling Practice and Theory, Volume 14, Issue 2, February 2006, Pages 126-142.

#### SCTUnit (beta)



- X-unit testing framework for YAKINDU Statechart Tools
- Test-driven development of Statechart models
- Test generation for various platforms
- Executable in YAKINDU Statechart Tools
- Virtual Time

```
Runs: 1/1 Errors: 0 Failures: 0

via org.yakindu.sct.LightSwitchTest [Runner: JUnit 4] (0,001 s)
initialStateIsOff (0,001 s)

testclass LightSwitchTest for statechart Light_Switch{
    @Test
    operation initialStateIsOff(){
        enter
        assert active(Light_Switch.main_region.Off)
    }
}
```

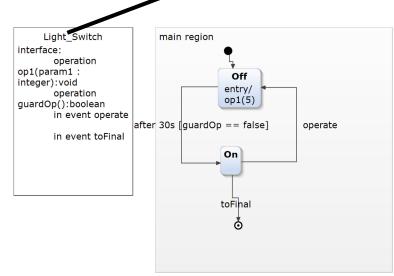






}

- Has a unique name
- Has a reference to a statechart
- Contains one or more operations





```
testsuite SomeTestSuite {
    someTestclass
}
```

- Has a unique name
- A testsuite contains at least one reference to a testclass



```
testclass someTestclass for statechart Light_Switch {
     @Test
     operation test(): void{
          enter
     }
}
```

- May have @Test or @Run annotation
- Has a unique name
- May have 0...n parameters
- Has a return type (standard is void)
- Contains 0...n statements





```
// entering / exiting the statechart
enter, exit
// raising an event
raise event : value
// proceeding time or cycles
proceed 2 cycle
proceed 200 ms
// asserting an expression, expression must evaluate to boolean
assert expression
// is a state active
active(someStatechart.someRegion.someState)
```



#### **Mocking Statements**

#### SCTUnit allows to

- mock operations defined in the statechart model
- verify that an operation was called with certain values

```
// mocking the return value of an operation mock mockOperation returns (20) mock mockOperation(5) returns (30) 
// verifying the call of an operation assert called verifyOperation with (5, 10)
```



#### **Control Structures**

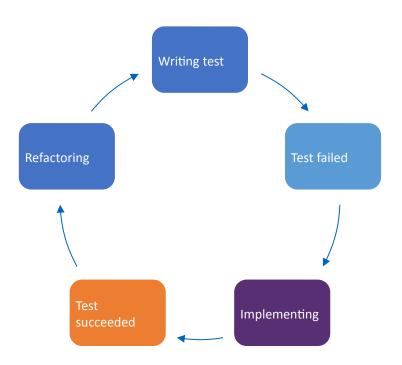
```
// if expression

if (x==5) {
    doSomething()
} else {
    doSomethingelse()
}
```

```
// while expression
while (x==5) {
   doSomething()
}
```

### Test-Driven Development

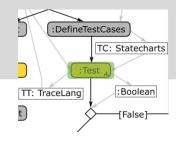
- Software development process, where software is developed driven by tests
- Test-first-approach
- 3 steps you do repeatedly:
  - writing a test
  - implementing the logic
  - refactoring

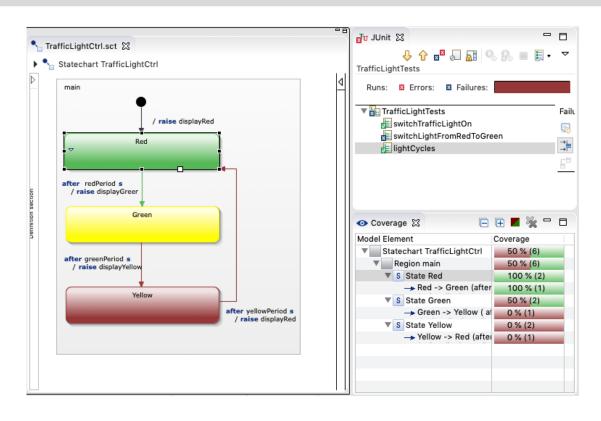


## Exercise 3

# Testing Models

### Exercise 3 – Unit testing Statecharts

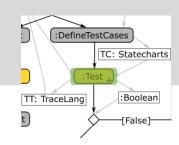




- Create a test that checks the following requirements:
  - R3: at system start-up, the red light is on
  - R4: cycles through red on, green on, and yellow on
  - R5: red is on for 60s, green is on for 55s, yellow is on for 5s

#### Exercise 3 – Solution

```
package trafficlight.test
testclass TrafficLightTests for statechart TrafficLightCtrl {
   @Test operation switchTrafficLightOn () {
        // given the traffic light is inactive
        assert !is active
       // when
        enter
       // then traffic light is off which means no color was switched on
        assert displayRed
        assert !displayGreen
        assert !displayYellow
   @Test operation switchLightFromRedToGreen () {
       // given
        switchTrafficLightOn
        // when
        proceed 60s
       // then
        assert displayGreen
   @Test operation switchLightFromGreenToYellow () {
        // given
        switchLightFromRedToGreen
        // when
        proceed 55s
        // then
        assert displayYellow
   @Test operation switchLightFromYellowToRed () {
        // given
        switchLightFromGreenToYellow
        // when
        proceed 5s
        // then
        assert displayRed
```

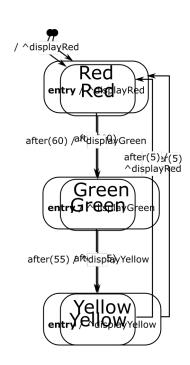


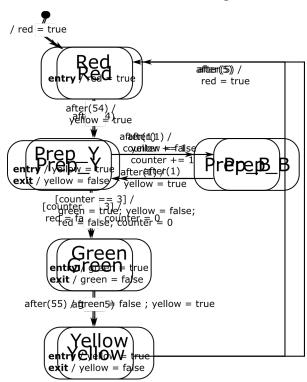
```
@Test operation lightCycles () {
    // given
    switchLightFromYellowToRed
    var i : integer = 10
    while (i > 0) {
        i=i-1
        //when
        proceed 60 s
        // then
        assert displayGreen
        //when
        proceed 55 s
        // then
        assert displayYellow
        //when
        proceed 5 s
        // then
        assert displayRed
```

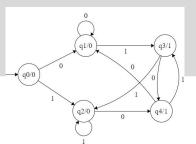
# Hierarchy

### **Entry/Exit Actions**

- A state can have entry and exit actions.
- An entry action is executed whenever a state is entered (made active).
- An exit action is executed whenever a state is exited (made inactive).
- Same expressiveness as transition actions (i.e., syntactic sugar).







#### **Transitions**



- Model the dynamics of the system:
  - when
    - the system is in state A and
    - event is received and
    - guard evaluates to true
  - then
    - 1. the exit actions of state A are evaluated and
    - 2. output\_action is evaluated and
    - 3. the enter actions of state B are evaluated and
    - 4. the new active state becomes B

### Entry/Exit Actions: Simulation Algorithm

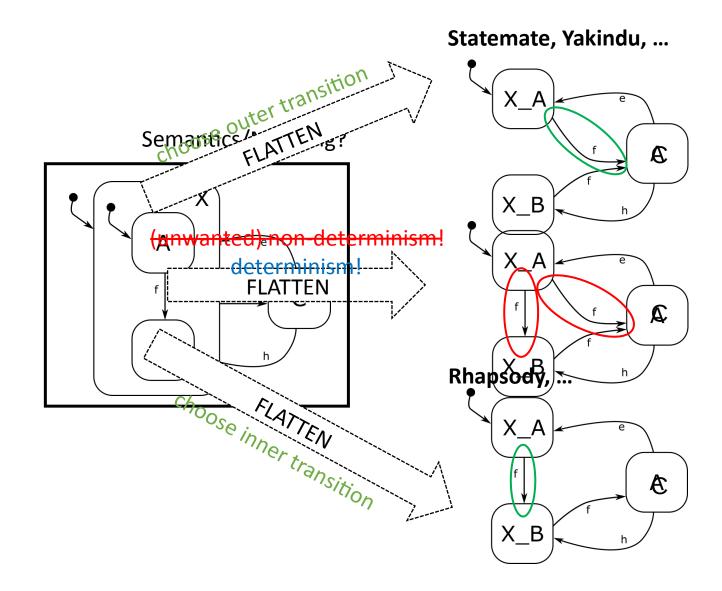
```
simulate(sc: Statechart) {
         input events = initialize queue()
        output events = initialize queue()
                      = initialize set()
        timers
         curr state
                      = sc.initial state
        for (var in sc.variables) {
            var.value = var.initial value
        while (not finished()) {
            curr event = input events.get()
10
            enabled transitions = find enabled transitions(curr state, curr event, sc.variables)
11
            while (not quiescent()) {
12
                 chosen transition = choose one transition(enabled transition)
13
                 cancel timers(curr state, timers)
14
                 execute exit actions(curr state)
15
                 curr state = chosen transition.target
16
                 chosen transition.action.execute(sc.variables, output_events)
17
                 execute enter actions(curr state)
18
                 start timers(curr state, timers)
19
                 enabled transitions = find enabled transitions(curr state, sc.variables)
20
21
22
23
24
```

### Hierarchy

- Statechart states can be hierarchically (de-)composed
- Each hierarchical state has exactly one initial/default state
- An active hierarchical state has exactly one active child (down to leaf/atomic state)

## 

### Hiearchy: Modified Example

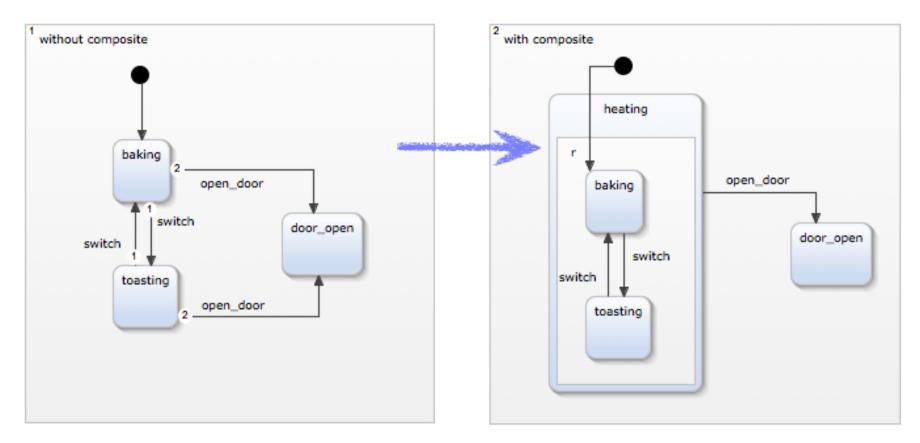


Hiearchy: why inner? ... see Code Generation

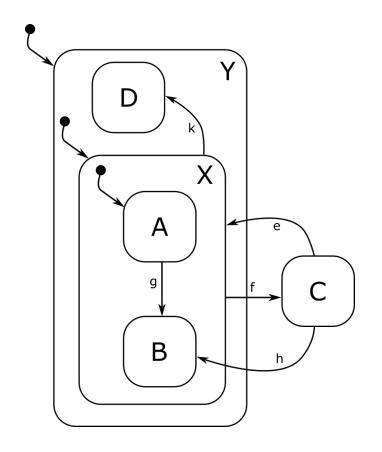


### **Composite States**

- Hierarchical states are an ideal mechanism for hiding complexity
- Parent states can implement common behaviour for their substates
- Hierachical event processing reduces the number of transitions
- Refactoring support: group states into a composite state

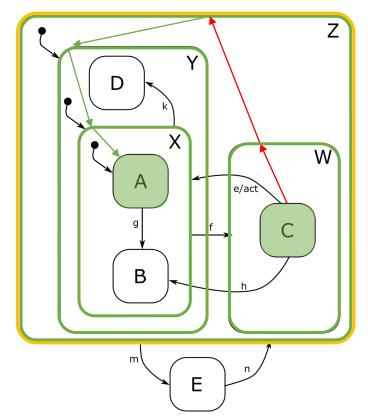


#### Hierarchy: Initialization



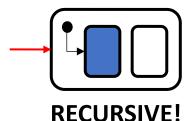
- Concept of effective target state
  - Recursive: the effective target state of a composite state is its initial state
- Effective target state of initial transition is Y/X/A
- Initialization:
  - 1. Enter Y, execute enter action
  - 2. Enter X, execute enter action
  - 3. Enter A, execute enter action

## Hierarchy: Transitions



- Assume Z/W/C is active and e is processed.
- Semantics:
  - 1. Find LCA, collect states to leave
  - 2. Leave states up the hierarchy
  - 3. Execute action *act*
  - 4. Find effective target state set, enter states down the hierarchy

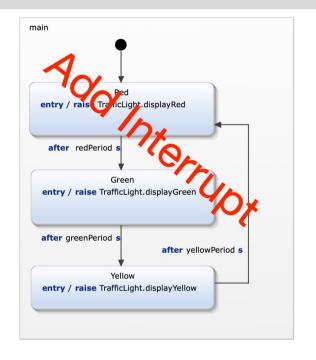
#### Effective target states:

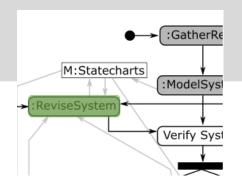


## Exercise 5

Model an interruptible traffic light

### Exercise 5 - Requirements

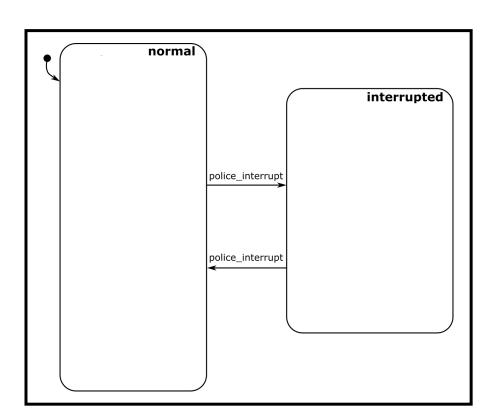


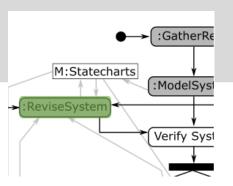


- R7a: police can interrupt autonomous operation .
- R7b: autonomous operation can be interrupted during any phase of constant red, yellow and green lights.
- R7c: in interrupted mode the yellow light blinks with a constant frequency of 1 Hz (on 0.5s, off 0.5s).
- R8a: police can resume to regular autonomous operation.
- R8b: when regular operation is resumed, the traffic light restarts with red (R) light on.

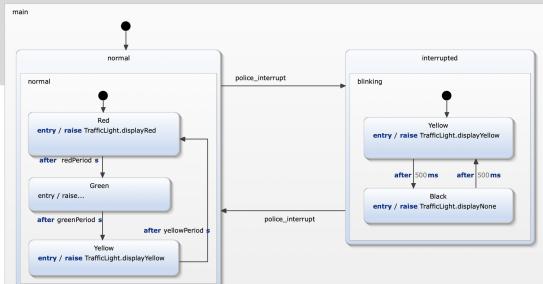
#### **Exercise 5: Solution**

- R7a: police can interrupt autonomous operation .
- R7b: autonomous operation can be interrupted during any phase of constant red, yellow and green lights.
- R7c: in interrupted mode the yellow light blinks with a constant frequency of 1 Hz (on 0.5s, off 0.5s).
- R8a: police can resume to regular autonomous operation.
- R8b: when regular operation is resumed, the traffic light restarts with red (R) light on.





### Exercise 5 - Solution



#### requirement

#### modelling approach

R6: police can interrupt autonomous operation.

An new incoming event police interrupt triggers a transition to a new state interrupted.

State interrupted is a composite state with two

R6a: autonomous operation can be interrupted during any phase of constant red, yellow and green lights.

The states Red, Green, and Yellow are grouped within a new composite state normal. This state is the source state of the transition to state interrupted and thus also applies to all substates.

R7: in interruptetd mode the yellow light blinks with a constant frequency of 1 Hz. (on 0.5s, off 0.5s).

light on and off. Timed transitions between these states ensure correct timing for blinking. A transition triggered by police interrupt leads from

substates Yellow and Black. These switch the yellow

When activating state normal its substate Red is activated by default.

state interrupted to state normal.

R8a: when regular operation is resumed the traffic light restarts with red (R) light on.

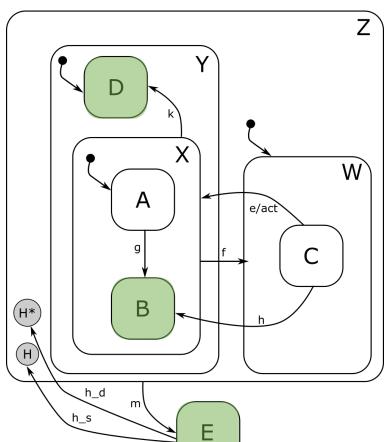
R8: police can resume to regular autonomous operation.

# History

### History: pseudo-states

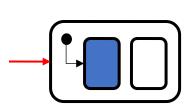


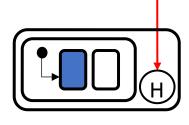


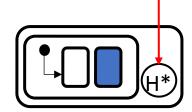


- Assume Z/Y/X/B is active, and m is processed
  - Effective target state: E
- If h\_s is processed
  - Effective target state:
     Z/Y/D
- If h\_d is processed
  - Effective target state:
     Z/Y/X/B

Effective target states:





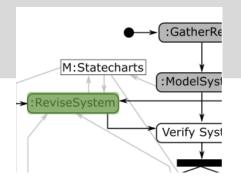


**RECURSIVE!** 

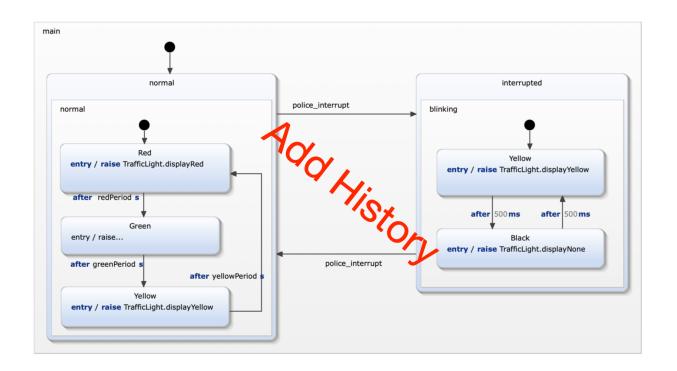
## Exercise 6

Model an interruptible traffic light that restores its state

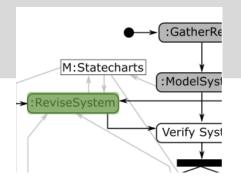
## Exercise 6: Requirements



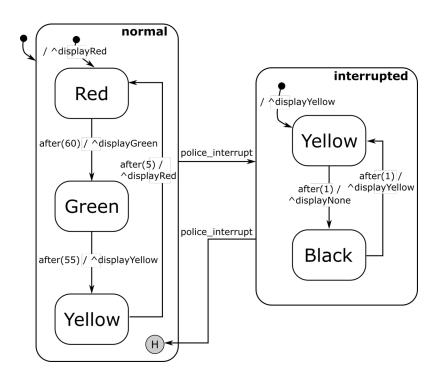
 R8b: when regular operation is resumed the traffic light restarts with the last active light color red (R), green (G), or yellow (Y) on.



#### Exercise 6: Solution



 R8b: when regular operation is resumed the traffic light restarts with the last active light color red (R), green (G), or yellow (Y) on.

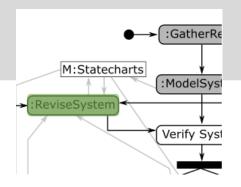


## Exercise 7

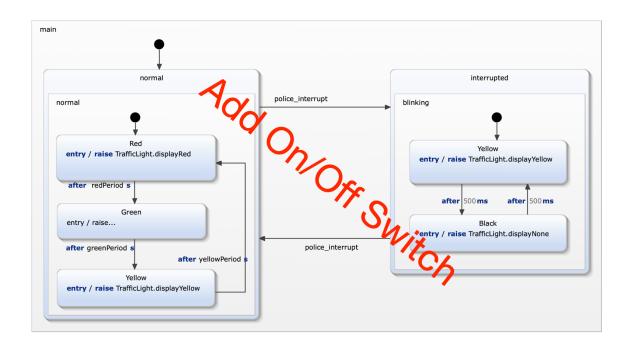
Model an interruptible traffic light that restores its state and can be switched on/off

### Exercise 7: Requirements

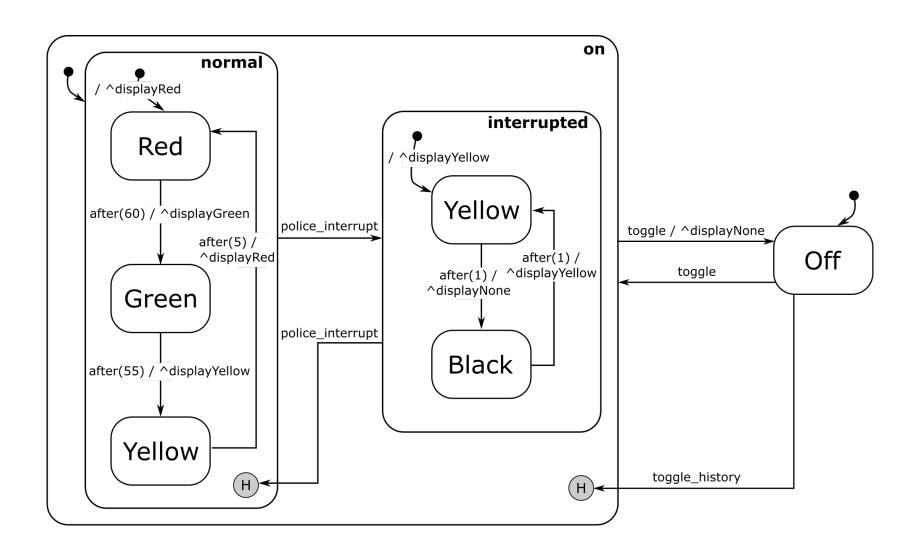
Add another level of hierarchy that supports switching on and off the entire traffic light. Go into detail with shallow and deep histories.



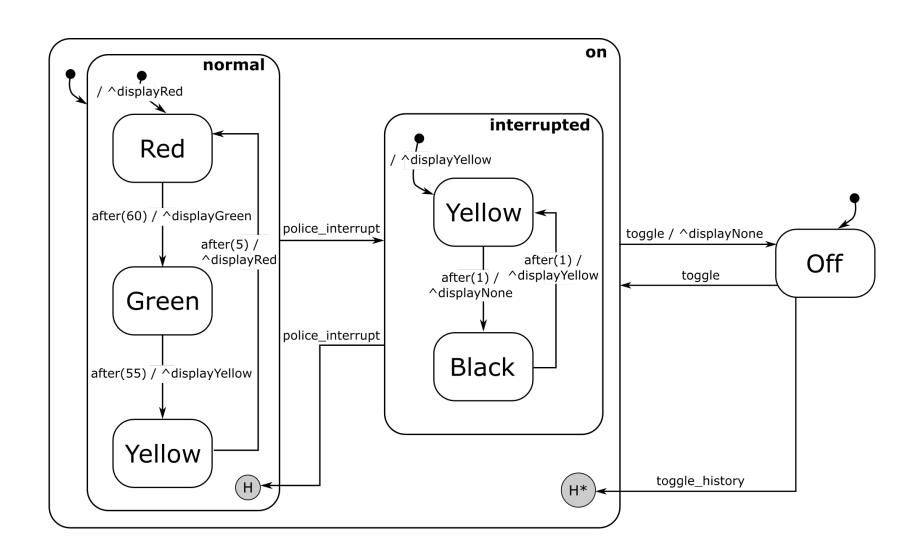
- R9: The traffic light can be switched on and off.
- R9a: The traffic light is initially off.
- R9b: If the traffic light is off none of its lights (R/G/Y) are on.
- R9c: After switching off and on again the traffic light must switch on the light that was on before the switching off.



#### Exercise 7: Solution



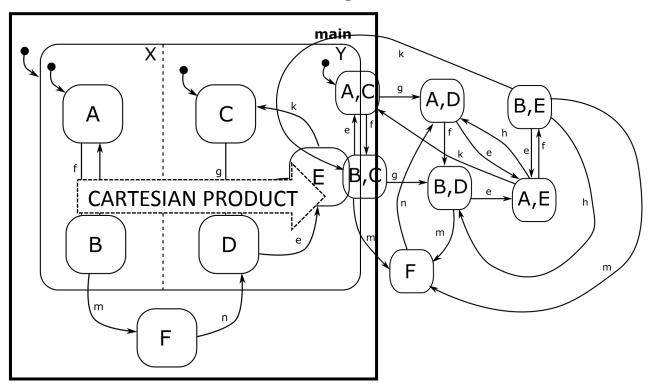
#### **Exercise 7: Alternative Solution**



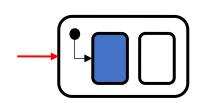
# Orthogonality

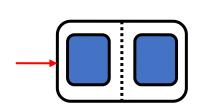
#### Orthogonal Components/Regions: "and" states

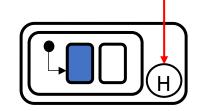
#### Semantics/Meaning?

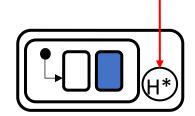


#### Effective target states:



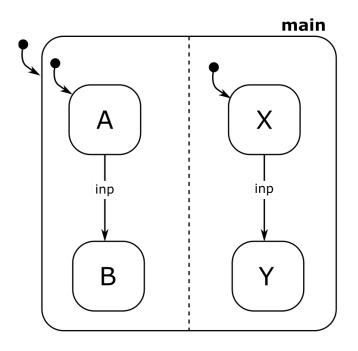


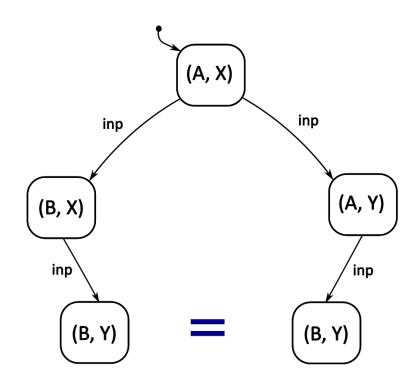




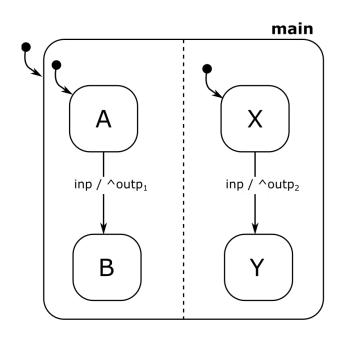
**RECURSIVE!** 

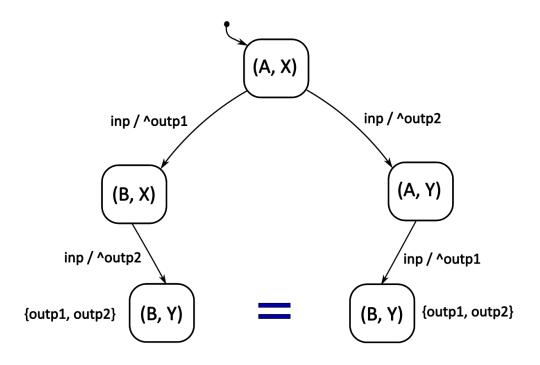
## Parallel (In)Dependence



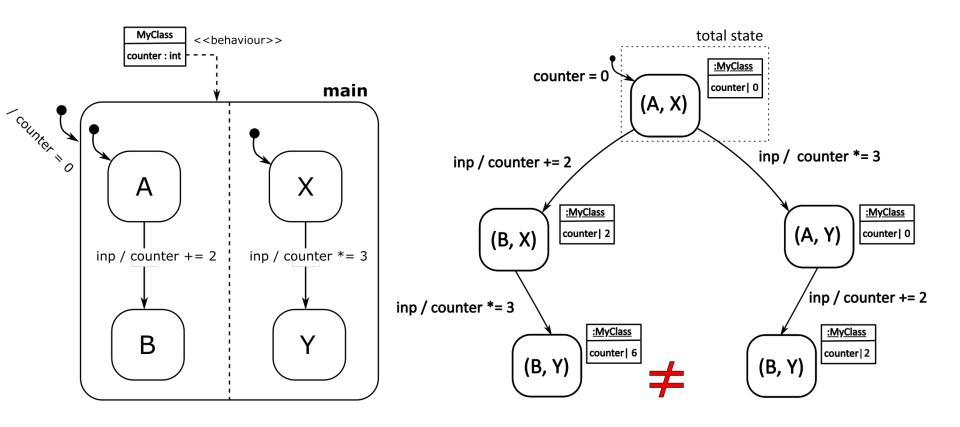


## Parallel (In)Dependence

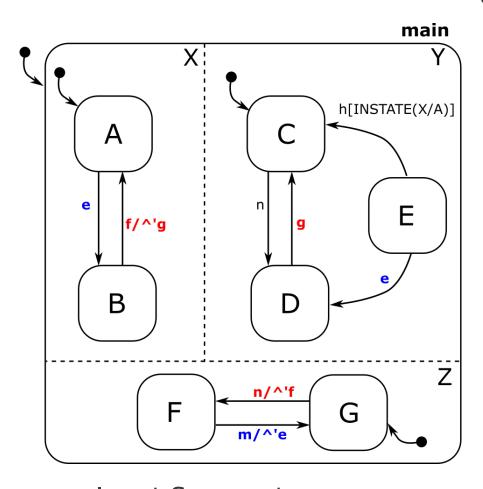




## Parallel (In)Dependence



#### Orthogonality: Communication



Input Segment: nmnn

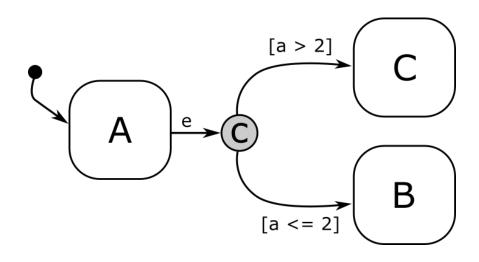
Orthogonal Components can communicate:

- raising/broadcasting local events:
   ^'<<event name>>
- state reference is a transition guard:INSTATE(<<state location>>)

### Simulation Algorithm

```
simulate(sc: Statechart) {
        input events = initialize queue()
        output events = initialize queue()
        local events = initialize queue()
                      = initialize set()
        timers
                      = get_effective_target_states(sc.initial_state)
        curr state
        for (var in sc.variables) {
            var.value = var.initial value
        while (not finished()) {
10
            curr event = input events.get()
11
            for (region in sc.orthogonal regions) {
12
                enabled transitions[region] = find enabled transitions(curr state, curr event, sc.variables)
13
14
15
            while (not quiescent()) {
                chosen_region = choose_one_region(sc.orthogonal_regions)
16
                chosen_transition = choose_one_transition(enabled_transition[chosen_region])
17
                states_to_exit = get_states_to_exit(get_lca(curr_state, chosen transition))
18
                for (state to exit in states to exit) {
19
                     cancel timers(state to exit, timers)
20
                     execute_exit_actions(state_to_exit)
21
22
                     remove state from curr state(state to exit)
23
                chosen transition.action.execute(sc.variables, output events, local events)
24
                states to enter = get effective target states(chosen transition)
25
                for (state to enter in states to enter) {
                     add state to curr state(state to enter)
27
                     execute enter actions(state to enter)
                     start timers(state to enter, timers)
29
30
                enabled_transitions = find_enabled_transitions(curr_state, sc.variables, local_events)
31
32
```

#### **Conditional Transitions**



- getEffectiveTargetStates(): select one True-branch
- Conditions should not overlap to avoid non-determinism
  - in Yakindu, priority makes deterministic
  - "else" branch is required
- Equivalent (in this case) to two transitions:
  - A e[a > 2] -> C
  - A e [a <= 2] -> B

## Exercise 8

Add a timer to the traffic light

### **Exercise 8: Requirements**

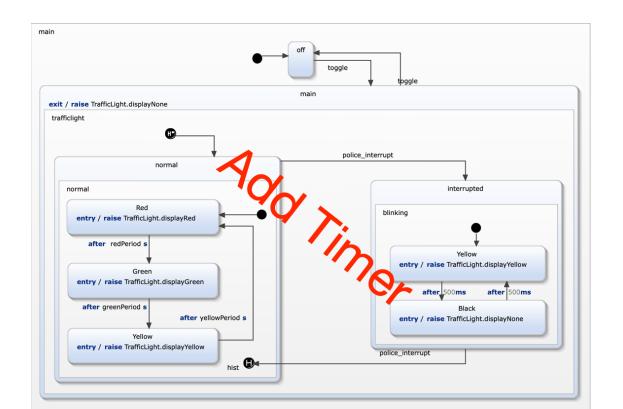
TrafficLight
- timer: int

M:Statecharts
:ModelSyst

Verify Syst

In this exercise a timer must be modelled. It introduces the use of orthogonal regions.

- R10a: A timer displays the remaining time while the light is red or green.
- R10b: This timer decreases and displays its value every second.
- R10c: The colour of the timer reflects the colour of the traffic light.

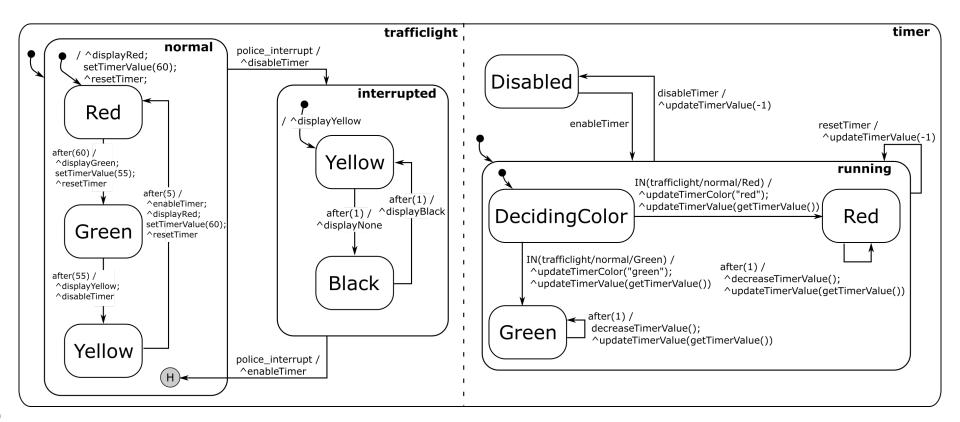


#### **Exercise 8: Solution**

- timer: int

-

- R10a: A timer displays the remaining time while the light is red or green.
- R10b: This timer decreases and displays its value every second.
- R10c: The colour of the timer reflects the colour of the traffic light.



#### Solution 8

#### requirement

#### modelling approach

R10: a timer displays the remaining time while the light is red or green

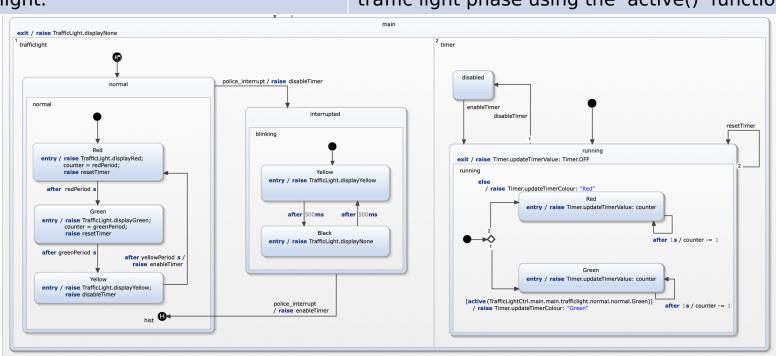
The timer is defined in a second region within state on (main in the Yakindu model).

R10a: This timer decreases and displays its value every second.

An internal variable for the counter is introduced. When switching the traffic light phase, the counter value is set to how long the light has been in that phase. Additionally, the local events resetTimer, enableTimer, and disableTimer are used to synchronize traffic light phase switches with the timer.

R10b: The colour of the timer reflects the colour of the traffic light.

When the timer is enabled it checks the active traffic light phase using the active() function.



\_

## Yakindu syntax

#### Yakindu:

- **raise** e == ^e
- strict alternation between "or" and "and" states

  TrafficLightCtrl.main.main.trafficlight.normal.normal.Green
- active() == INSTATE() == IN()

1



- Code generators for C, C++, Java, Python, Swift, Typescript, SCXML
- Plain-code approach by default
- Very efficient code
- Easy integration of custom generators

















- Various different approaches for implementing a state machine:
  - switch / case
  - state transition table
  - state pattern
- Which one is the best depends on
  - Runtime (performance, predictability) requirements
  - ROM vs. RAM memory
  - Debugging capabilities
  - Clarity and maintainability (of generated code ~ certification, round-trip)

14

#### Switch / Case

 Each state corresponds to one "case"

 Each case executes state-specific statements and state transitions

```
public void stateMachine() {
        while (true) {
            switch (activeState) {
            case RED: {
                activeState = State.RED_YELLOW;
                break;
            case RED YELLOW: {
                activeState = State.GREEN;
                break:
            case GREEN: {
                activeState = State.YELLOW;
                break;
            case YELLOW: {
                activeState = State.RED;
                break;
```

.

#### State Transition Table

- Specifies the state machine purely declaratively.
- One of the dimensions indicates current states, while the other indicates events.

16

#### State Pattern

- Object-oriented implementation, behavioural design pattern
- Used by frameworks such as Spring Statemachine, Boost MSM or Qt State Machine Framework
- Each State becomes a class, events become methods
- All classes derive from a common interface

```
public class MovingUp extends AbstractState {
   public MovingUp(StateMachine stateMachine) {
        super(stateMachine);
   }

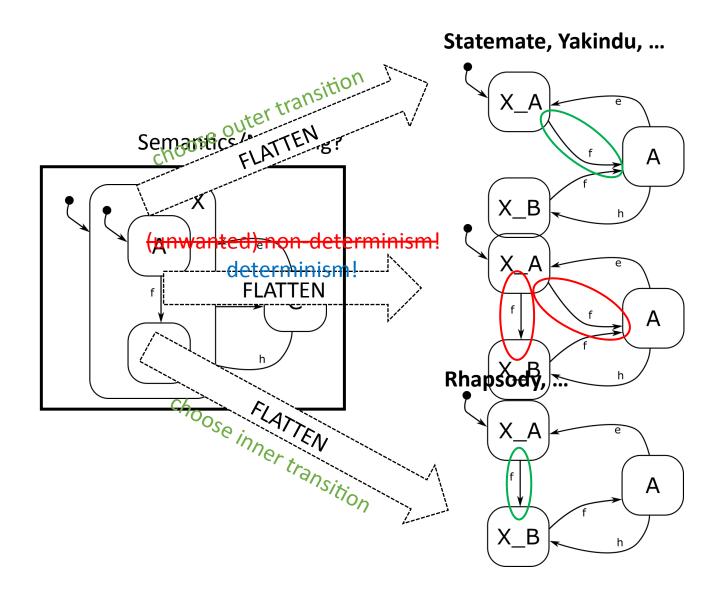
   @Override
   public void raiseUserDown() {
        stateMachine.activateState(new Idle(stateMachine));
   }

   @Override
   public void raisePosSensorUpperPosition() {
        stateMachine.activateState(new Idle(stateMachine));
   }

   @Override
   public String getName() {
        return "Moving up";
   }
}
```

7

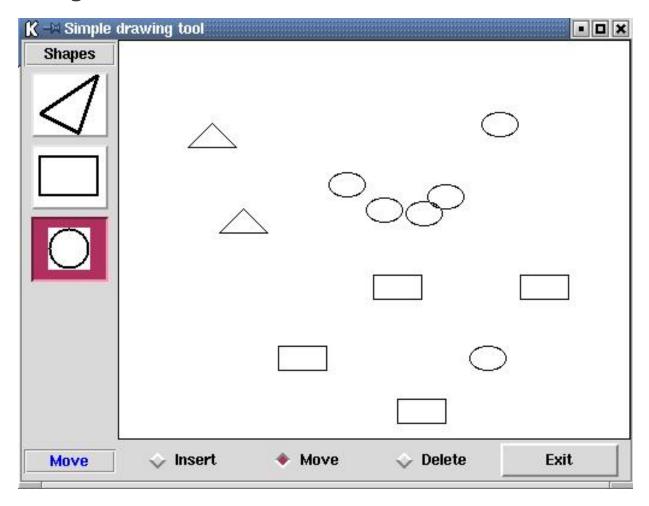
## Hierarchy: outer vs. inner



.0

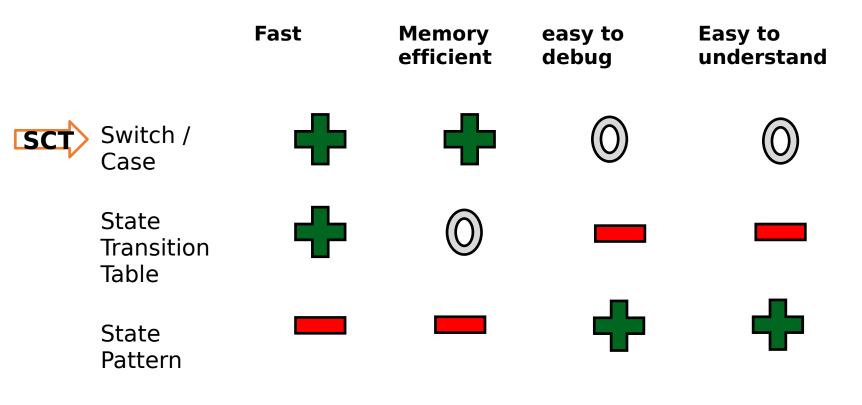
### Hierarchy: why inner?

Manual Object-Oriented implementation (no Statechart compiler) using State Pattern



http://msdl.cs.mcgill.ca/people/hv/teaching/SoftwareDesign/COMP304B2002/lectures/lecture.statecharts/

19



very simplified illustration

0



#### Code Generator Model

```
GeneratorModel for Vakindu::java {
    statechart exercise5 {

        feature Outlet {
            targetProject = "5_sctunit"
            targetFolder = "src-gen"
            libraryTargetFolder = "src"
        }
    }
}
```

- Has a generator ID
- Has a generator entry
- Each generator entry contains 1..n feature-configurations
- Each feature-configuration contains 1..n properties

#### Generated Code

#### Sample

#### **Files**

- ✓ ₾ src-gen
   ✓ ➡ traffic.light
   ✓ ➡ trafficlightctrl
   → ☑ ITrafficLightCtrlStatemachine.java
   → ☑ SynchronizedTrafficLightCtrlStatemachine.java
   → ☑ TrafficLightCtrlStatemachine.java
   → ☑ IStatemachine.java
   → ☑ ITimer.java
   → ☑ ITimerCallback.java
   → ☑ RuntimeService.java
   → ☑ TimerService.java
- 8 files
- > 1311 lines of code
- 302 manual (UI) code

```
TrafficLightCtrl.sct
                   🚺 TrafficLightCtrlStatemachine.java 🔀
             break;
         case main_main_trafficlight_interrupted_blinking Yellow:
             exitSequence_main_main_trafficlight_interrupted_blinking_Yellow();
             break;
         case main_main_trafficlight_normal_normal_Red:
             exitSequence main main trafficlight normal normal Red();
         case main main trafficlight normal normal Yellow:
             exitSequence main main trafficlight normal normal Yellow();
             break;
         case main_main_trafficlight_normal_normal_Green:
             exitSequence_main_main_trafficlight_normal_normal_Green();
             break;
         default:
             break;
     /* Default exit sequence for region blinking */
     private void exitSequence main main trafficlight interrupted blinking() {
         switch (stateVector[0]) {
         case main_main_trafficlight_interrupted_blinking Black:
             exitSequence main main trafficlight interrupted blinking Black();
         case main_main_trafficlight_interrupted_blinking_Yellow:
             exitSequence_main_main_trafficlight_interrupted_blinking_Yellow();
             break;
         default:
             break;
     /* Default exit sequence for region normal */
     private void exitSequence main main trafficlight normal normal() {
         switch (stateVector[0]) {
         case main_main_trafficlight_normal_normal_Red:
             exitSequence main main trafficlight normal normal Red();
         case main_main_trafficlight_normal_normal_Yellow:
             exitSequence main main trafficlight normal normal Yellow();
         case main_main_trafficlight_normal_normal_Green:
             exitSequence main main trafficlight normal normal Green();
             break;
         default:
             break;
     /* Default exit sequence for region timer */
     private void exitSequence main main timer() {
          cuitch (ctato)/octon[1]) [
```

#### **Interface**

#### TrafficLightCtrl interface: in event police\_interrupt in event toggle interface TrafficLight: out event displayRed out event displayGreen out event displayYellow out event displayNone interface Timer: out event updateTimerColour: string out event updateTimerValue: integer internal: event resetTimer event disableTimer event enableTimer var counter: integer

# Setup Code (Excerpt)

#### Generator

```
GeneratorModel for yakindu::java {
    statechart TrafficLightCtrl {
        feature Outlet {
            targetProject = "traffic light history"
            targetFolder = "src-gen"
        feature Naming {
            basePackage = "traffic.light"
            implementationSuffix =""
        feature GeneralFeatures {
            RuntimeService = true
            TimerService = true
            InterfaceObserverSupport = true
        feature SynchronizedWrapper {
            namePrefix = "Synchronized"
            nameSuffix =
```

```
protected void setupStatemachine() {
    statemachine = new SynchronizedTrafficLightCtrlStatemachine();
    timer = new MyTimerService(10.0);
    statemachine.setTimer(timer);
    statemachine.getSCITrafficLight().getListeners().add(new ITrafficLightCtrlStatemachine.SCITrafficLightListener() {
        public void onDisplayYellowRaised() {
            setLights(false, true, false);
        public void onDisplayRedRaised() {[]
        public void onDisplayNoneRaised() {
        public void onDisplayGreenRaised() {[...
    });
    statemachine.getSCITimer().getListeners().add(new ITrafficLightCtrlStatemachine.SCITimerListener() {
        public void onUpdateTimerValueRaised(long value) {
            crossing.getCounterVis().setCounterValue(value);
        @Override
        public void onUpdateTimerColourRaised(String value) {
            crossing.getCounterVis().setColor(value == "Red" ? Color.RED : Color.GREEN);
    });
    buttonPanel.getPoliceInterrupt()
            .addActionListener(e -> statemachine.getSCInterface().raisePolice interrupt());
    buttonPanel.getSwitchOnOff()
            .addActionListener(e -> statemachine.getSCInterface().raiseToggle());
    statemachine.init();
private void setLights(boolean red, boolean yellow, boolean green) {
    crossing.getTrafficLightVis().setRed(red);
    crossing.getTrafficLightVis().setYellow(yellow);
    crossing.getTrafficLightVis().setGreen(green);
    repaint();
```

#### Runner

```
protected void run() {
    statemachine.enter();
    RuntimeService.getInstance().registerStatemachine(statemachine, 100);
}
```

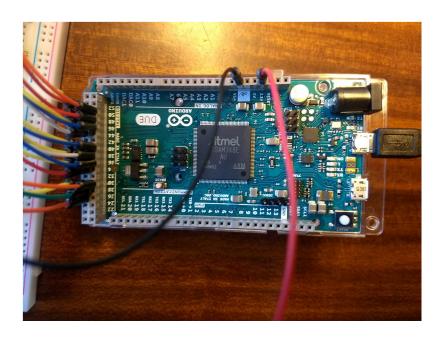
## Deployed Application (Scaled Real-Time)

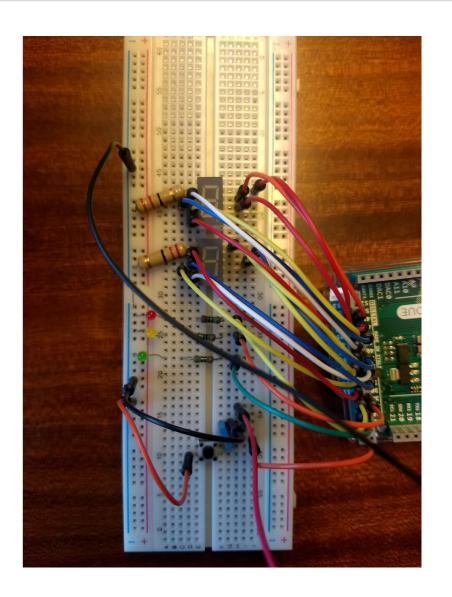


## Deploying onto Hardware

#### Interface:

- pinMode(pin\_nr, mode)
- digitalWrite(pin\_nr, {0, 1})
- digitalRead(pin\_nr): {0, 1}



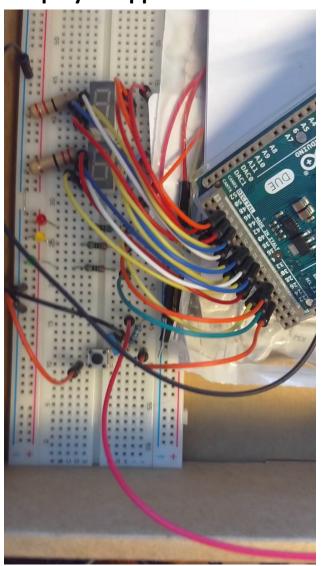


## Deploying onto Hardware

#### Runner

```
#define CYCLE PERIOD (10)
 static unsigned long cycle_count = 0L;
 static unsigned long last_cycle_time = 0L;
 void loop() { Generator
   read_pushbut Generator Model for yakindu::c {
   if ( cycle c
                                                                      PERIOD) ) {
                   statechart TrafficLightCtrl {
     sc timer s
                                                                      .e time);
     synchroniz
                       feature Outlet {
     trafficLig
                           targetProject = "traffic_light_arduino"
     last cycle
                           targetFolder = "src-gen"
     cycle coun
                           libraryTargetFolder = "src-gen"
                       feature FunctionInlining {
                           inlineReactions = true
Button Co
                           inlineEntryActions = true
                           inlineExitActions = true
void read pushk
                           inlineEnterSequences = true
 int pin value
                           inlineExitSequences = true
                           inlineChoices = true
 if (pin value
                           inlineEnterRegion = true
    button->las
                           inlineExitRegion = true
                           inlineEntries = true
 if ((millis()
                                                                      ay) {
    if (pin val
      button->s
      button->( }
  button->debounce state = pin value;
```

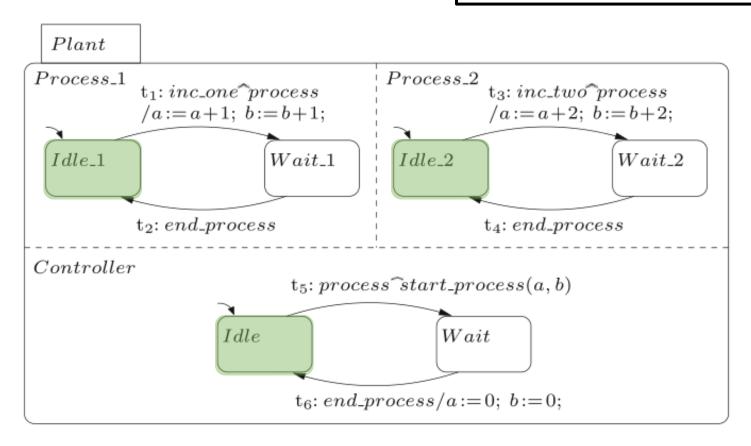
#### **Deployed Application**



# Semantic Choices

#### Semantic Choices

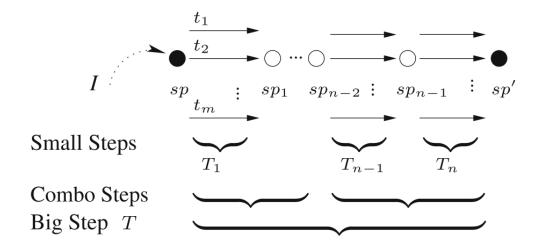
enabled events: [inc\_one, inc\_two]



Esmaeilsabzali, S., Day, N.A., Atlee, J.M. et al., Deconstructing the semantics of big-step modelling languages, Requirements Eng (2010) 15: 235.

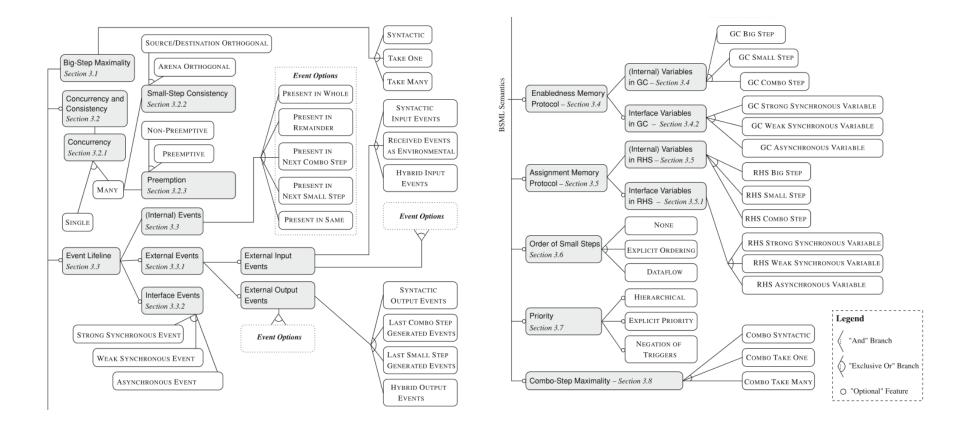
## Big Step, Small Step

- A "big step" takes the system from one "quiescent state" to the next.
- A "small step" takes the system from one "snapshot" to the next (execution of a set of enabled transitions).
- A "combo step" groups multiple small steps.



Esmaeilsabzali, S., Day, N.A., Atlee, J.M. et al., Deconstructing the semantics of big-step modelling languages, Requirements Eng (2010) 15: 235.

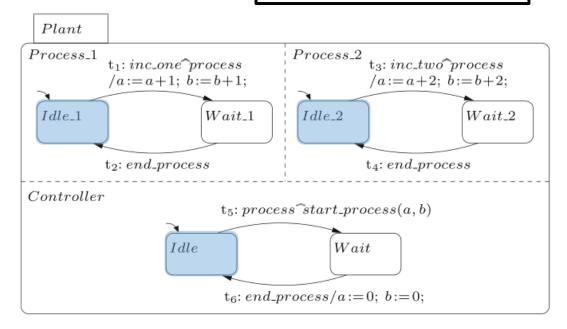
## Semantic Options



Esmaeilsabzali, S., Day, N.A., Atlee, J.M. et al., Deconstructing the semantics of big-step modelling languages, Requirements Eng (2010) 15: 235.

## Revisiting the Example

#### enabled events: [inc one, inc two]



concurrency: single

event lifeline: next combo step

assignment: RHS small step

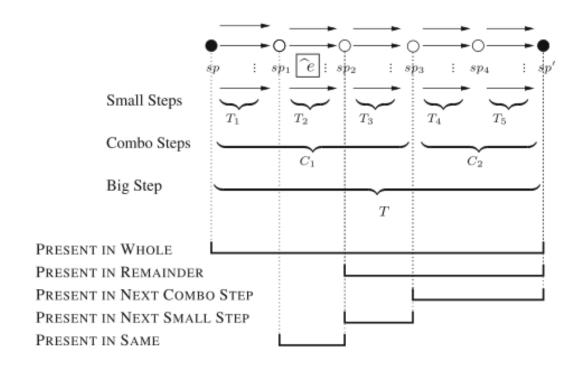
$$-><\{t1\}, \{t3\}, \{t5\}>$$
 and

event lifeline: present in remainder

possible

Esmaeilsabzali, S., Day, N.A., Atlee, J.M. et al., Deconstructing the semantics of big-step modelling languages, Requirements Eng (2010) 15: 235.

### **Event Lifeline**



## Semantic Options: Examples

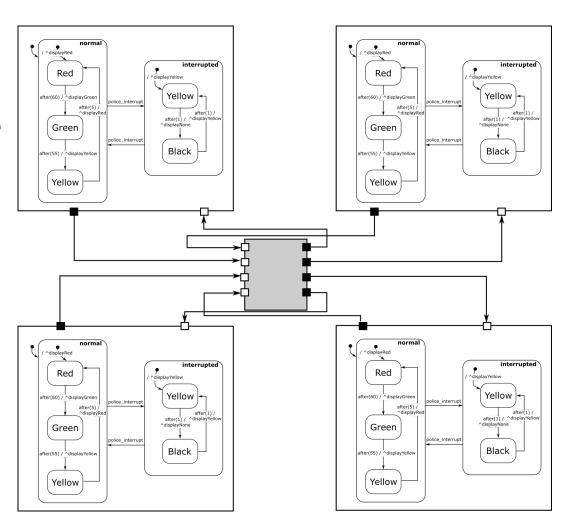
|                         | Rhapsody         | Statemate        | (Default) SCCD   |
|-------------------------|------------------|------------------|------------------|
| Big Step Maximality     | Take Many        | Take Many        | Take Many        |
| Internal Event Lifeline | Queue            | Next Combo Step  | Queue            |
| Input Event Lifeline    | First Combo Step | First Combo Step | First Combo Step |
| Priority                | Source-Child     | Source-Parent    | Source-Parent    |
| Concurrency             | Single           | Single           | Single           |

Simon Van Mierlo, Yentl Van Tendeloo, Bart Meyers, Joeri Exelmans, and Hans Vangheluwe. SCCD: SCXML extended with class diagrams. In 3rd Workshop on Engineering Interactive Systems with SCXML, part of EICS 2016, 2016.

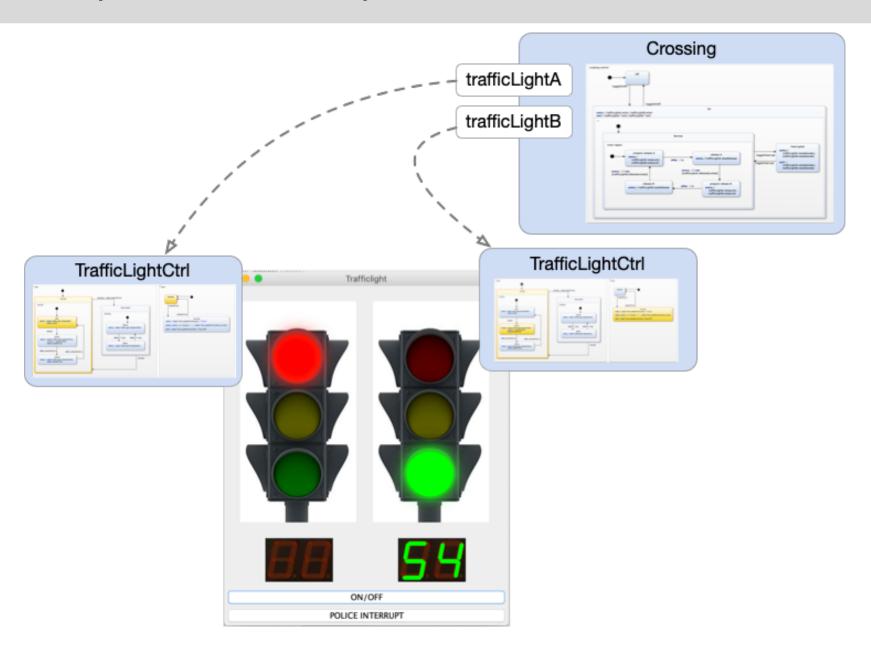
# Composition

## Composition of Statecharts

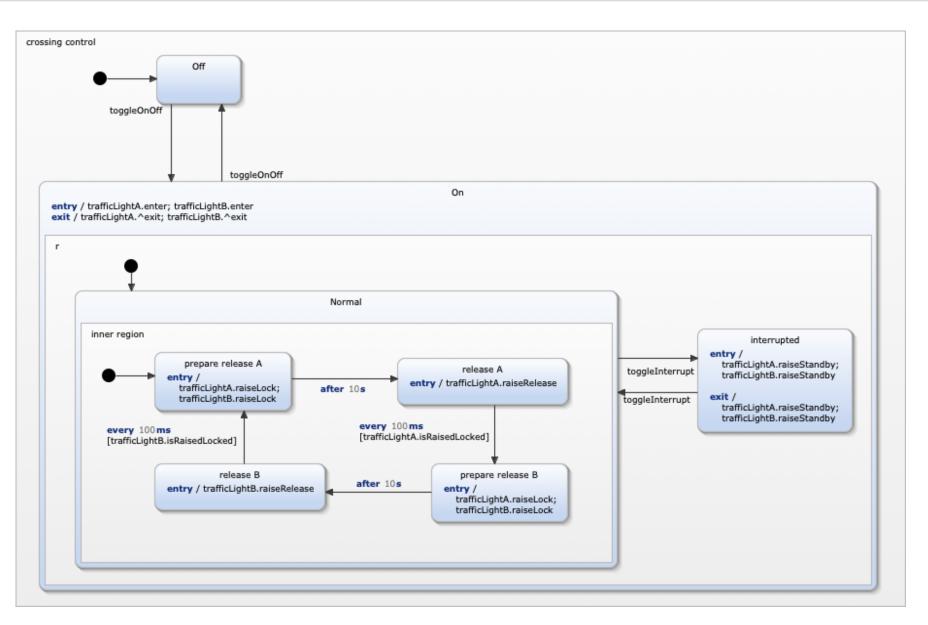
- Composition of multiple Statechart models
  - Instantiation
  - Communication
  - Semantics
- Often solved in code...



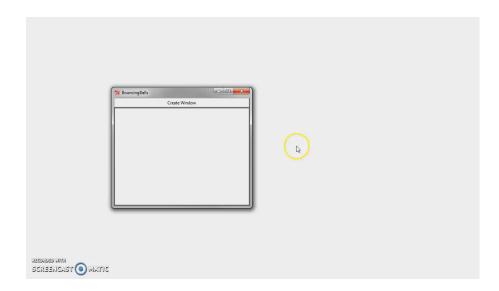
## Composition Example



## Composition Example



## Dynamic Structure: SCCD



#### **Behavior**

- Timed
- Autonomous
- Interactive
- Hierarchical

#### Structure

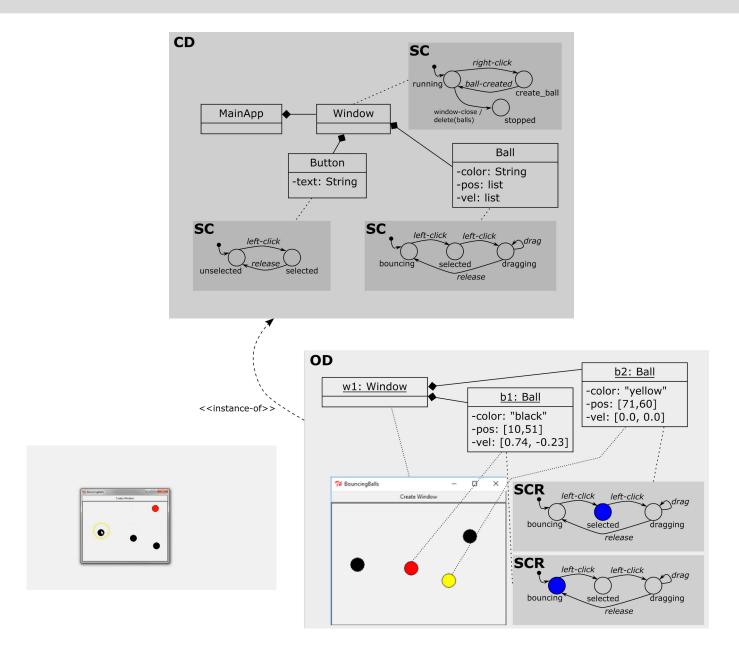
- Dynamic
- Hierarchical

Design? Statecharts + ???

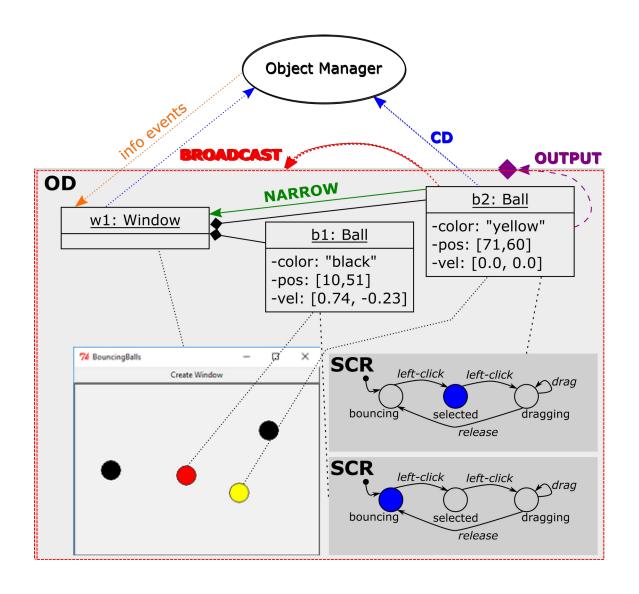
Coordination/Communication/Dynamic Structure often implemented in code...

Simon Van Mierlo, Yentl Van Tendeloo, Bart Meyers, Joeri Exelmans, and Hans Vangheluwe. SCCD: SCXML extended with class diagrams. In 3rd Workshop on Engineering Interactive Systems with SCXML, part of EICS 2016, 2016.

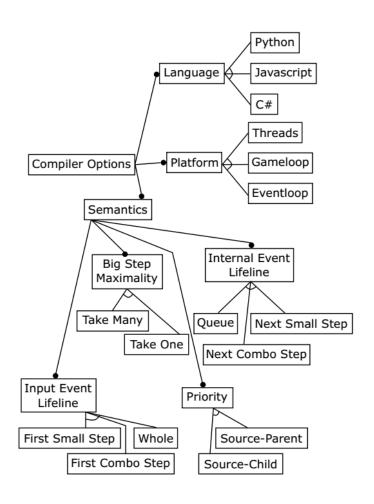
#### SCCD: Conformance

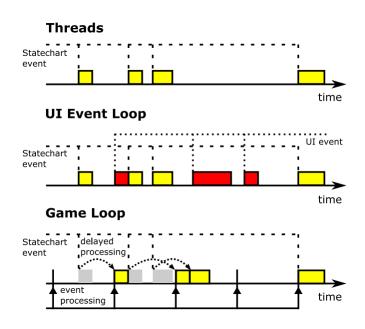


## Communication: Event Scopes



## SCCD Compiler





Simon Van Mierlo, Yentl Van Tendeloo, Bart Meyers, Joeri Exelmans, and Hans Vangheluwe. **SCCD: SCXML extended with class diagrams**. In *3rd Workshop on Engineering Interactive Systems with SCXML, part of EICS 2016*, 2016

## https://msdl.uantwerpen.be/documentation/SCCD/

#### SCCD Documentation

SCCD [SCCD] is a language that combines the Statecharts [Statecharts] language with Class Diagrams. It allows users to model complex, timed autonomous, reactive, dynamic-structure systems.

The concrete syntax of SCCD is an XML-format loosely based on the W3C SCXML recommendation. A conforming model can be compiled to a number of programming languages, as well as a number of runtime platforms implemented in those languages. This maximizes the number of applications that can be modelled using SCCD, such as user interfaces, the artificial intelligence of game characters, controller software, and much more.

This documentation serves as an introduction to the SCCD language, its compiler, and the different supported runtime platforms.

#### Contents

- Installation
  - Download
  - Dependencies
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- · Language Features
  - · Top-Level Elements
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  - Statechart Concepts
  - Executable Content
  - Macros
  - Object Manager
- Compiler
- · Runtime Platforms

  - Threads
  - Eventloop
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- Examples
  - Timer
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  - Priority
  - Concurrency
- Socket Communication
  - Initialization
  - Input Events
  - Output Events
  - HTTP client/server
- · Internal Documentation
  - · Statecharts Core

#### References

[SCCD] Simon Van Mierlo, Yentl Van Tendeloo, Bart Meyers, Joeri Exelmans, and Hans Vangheluwe. SCCD: SCXML extended with class diagrams. In 3rd Workshop on Engineering Interactive Systems with SCXML, part of EICS 2016, 2016. [LINK]

[Statecharts] David Harel, Statecharts: A visual formalism for complex systems, Sci. Comput. Program, 8, 3 (1987), 231–274. [LINK]

## Recap

- Model the behaviour of complex, timed, reactive, autonomous systems
  - "What" instead of "How"
     (= implemented by Statecharts compiler)
- Abstractions:
  - States (composite, orthogonal)
  - Transitions
  - Timeouts
  - Events
- Tool support:
  - Yakindu
  - SCCD