



**Design Automation & Test in Europe**  
 20-24 April 2009 - Nice, France

*Call for Posters*

The European Event for Electronic System Design & Test

DATE'09 Workshop Call for Posters

08:30 - 17:00, April 24, 2009

## Designing for Embedded Parallel Computing Platforms: Architectures, Design Tools, and Applications

Embedded systems designers are increasingly faced with the challenge of designing concurrent systems, both as multi-core architectures as well as heterogeneous platforms (e.g., DSP and FPGA). Designers need to take advantage of the parallelism on offer, but there are significant issues:

- How can we create applications that can exploit the underlying (parallel) architecture?
- What does the application designer really need to know of the underlying architecture?
- What tools are needed to write applications and what part of the process should they automate?
- How should we design the underlying hardware architectures?

This workshop convenes embedded parallel system researchers to address these and related issues.

### TOPIC AREA FOR SUBMISSIONS



The workshop will have three main sessions:

- Architectures: on the most relevant problems arising during the design optimization and exploration of many/multi core architectures.
- Design tools: on the state-of-the-art of tool development, showing where we are now and the directions we need to move in.
- Applications: on the analysis, development, modification and integration of applications with respect to parallel computing platforms.

### ORGANIZATION

**General Chairs:** Mike Woodward and Pieter J. Mosterman, *The MathWorks*, USA

[Mike.Woodward|Pieter.Mosterman]@MathWorks.com

**Architecture Session Chair**

Cristina Silvano  
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**Design Tools Session Chair**

Maryse Wouters  
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**Applications Session Chair**

Michael Hübner  
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### SUBMISSION INSTRUCTIONS

We invite posters for each of the three workshop sessions. In addition to the poster sessions, there will be a short presentation time during the workshop. Submissions should either be a 150-200 word abstract or in the form of the poster itself (in an A4 or US Letter size format) and should clearly identify the relevant session (architectures, design tools, or applications). Submissions must be sent to Pieter Mosterman (Pieter.Mosterman@MathWorks.com).

Submission deadline February 13, 2009

Notification of acceptance February 27, 2009

Sponsored by the European Design and Automation Association, the EDA Consortium, the IEEE Computer Society. (TTTC), (CEDA), ECSI, RAS and ACM SIGDA.

