

Multi-granularity NoC simulation framework for early phase exploration of SDR platforms



Nikolaos Zompakis¹, Martin Trautmann², Alexandros Bartzas³, Stylianos Mamagkakis², Dimitrios Soudris¹, Liesbet Van der Perre², Francky Catthoor²

¹ ECE School, National Technical Univ. of Athens, 15780 Zografou, Greece ² IMEC vzw, Kapeldreef 75, 3001 Heverlee, Belgium ³ ECE Department, Democritus Univ. of Thrace, 67100 Xanthi, Greece

Abstract

The SDR system can dynamically adopt appropriate modes to get the optimal quality of the communication service [1]

The run-time resource management optimizations can reduce resource requests without affecting significantly the Quality of Service (QoS) and the interaction between user and application [2]

Our framework combined with a cycle-accurate NoC [3] simulation environment that enables the simulation of such complex, dynamic hardware/software SDR designs

The platform specifications are represented as a virtual architecture by a coarse-grain simulator described in SystemC

 Key of our approach – our tool is supported by automatic wrappers which explore the SDR platform parameters and transmit the interconnection traffic in a cycle-accurate NoC simulator giving the opportunity to examine the impact of different topologies at the system bandwidth at execution time

This is work partially supported by the EC funded MOSART project http://www.mosart-project.org/

1. SDR exploration space

Pe_Fr (MHz)		Modulation	#PE		Symbol rate (ns/OFDM_sym bols)	Packet Size in Bytes
400,	200,	QAM16,	2,4,	2, 4	1000,	1000,
600,	400	QPSK,	6,8		1500,	1500.
800	600	BPSK			2000	2000

Simulation results of SDR platform parameters exploration on a 4x4 2D torus NoC







4. Experimental Results

0,334 1,1/1 (6bps 0 486 Gb



0,982 Gbp:

0,802 Gb





References

[1], David L. Tennenhouse and Vanu G. Bose, "The Spectrumware Approach to Wireless Signal Processing," Wireless Network .Journal, 2(1),1996.

[2]. B. Bougard et al., "Cross-layer power management in wireless networks and consequences on system-level architecture," in Signal Processing, Vol. 86, No.8, p. 1792-1803, 2006.

[3]. A. Jantsch, "Models of computation for networks on chip," in Proc. ACSD'06, pp. 165-178, 2006

Simulation results of SDR platform parameters exploration on a 4x4 Mesh NoC





exploration on a 4x4 1D torus NoC

Simulation results of SDR platform parameters